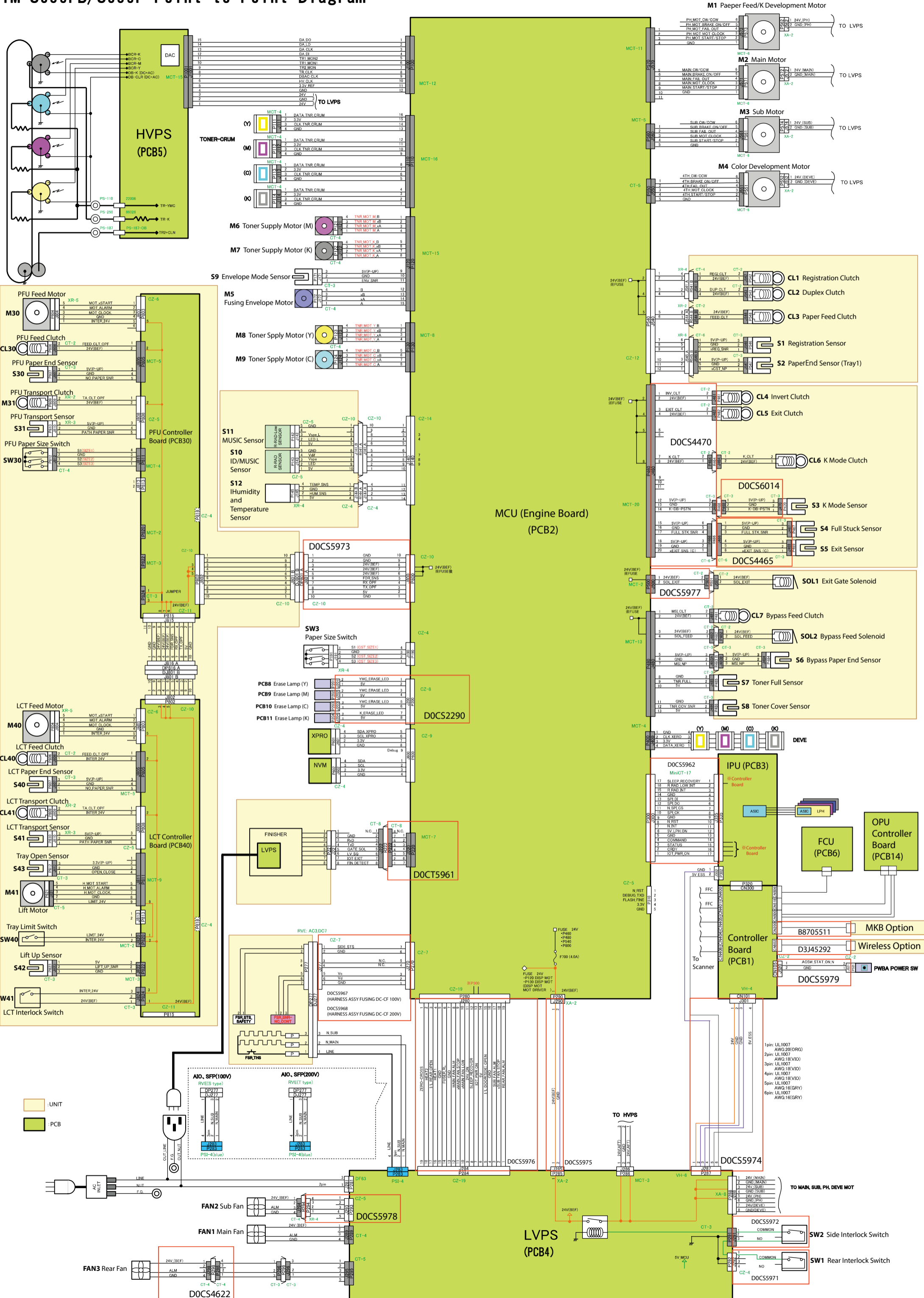
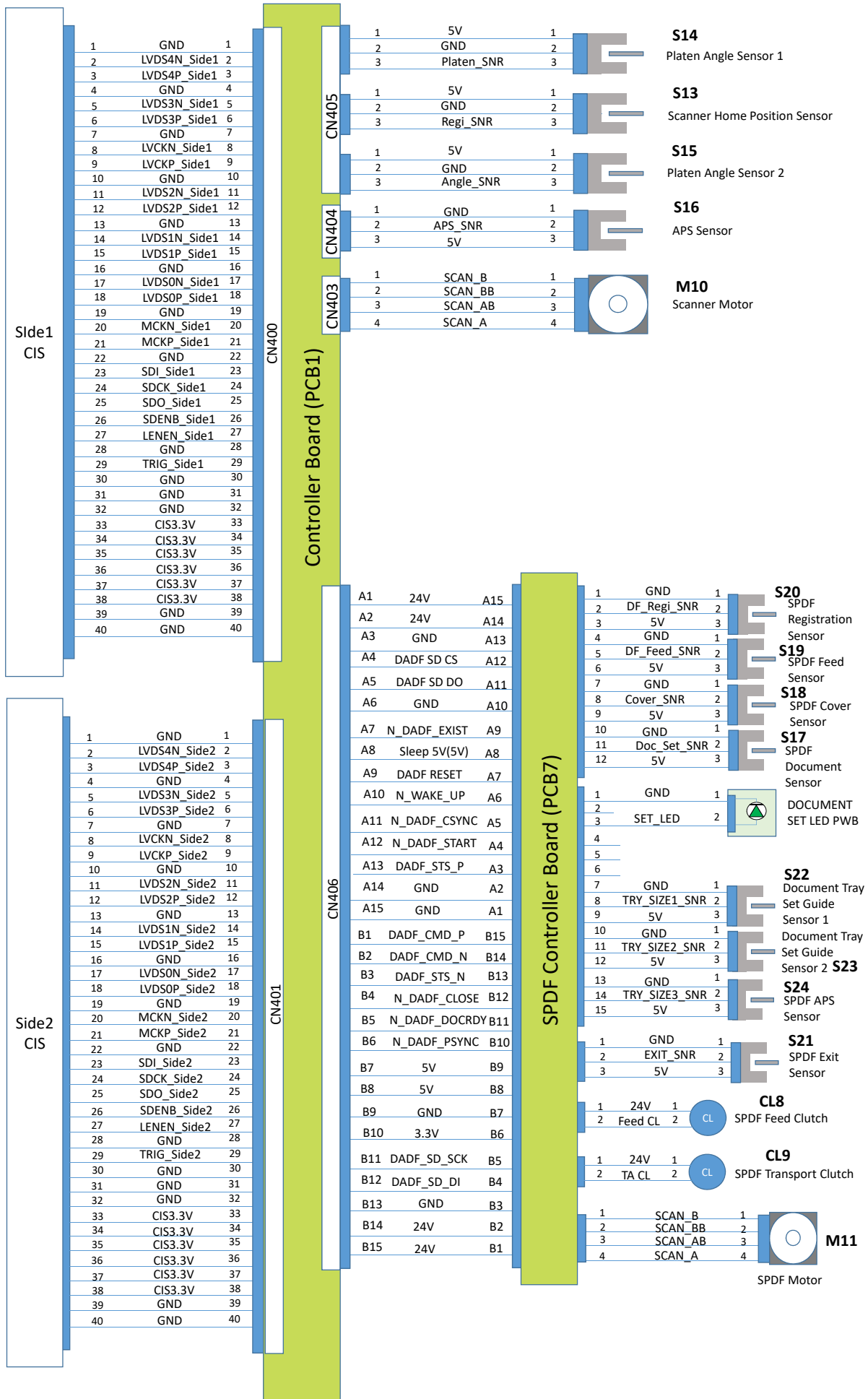
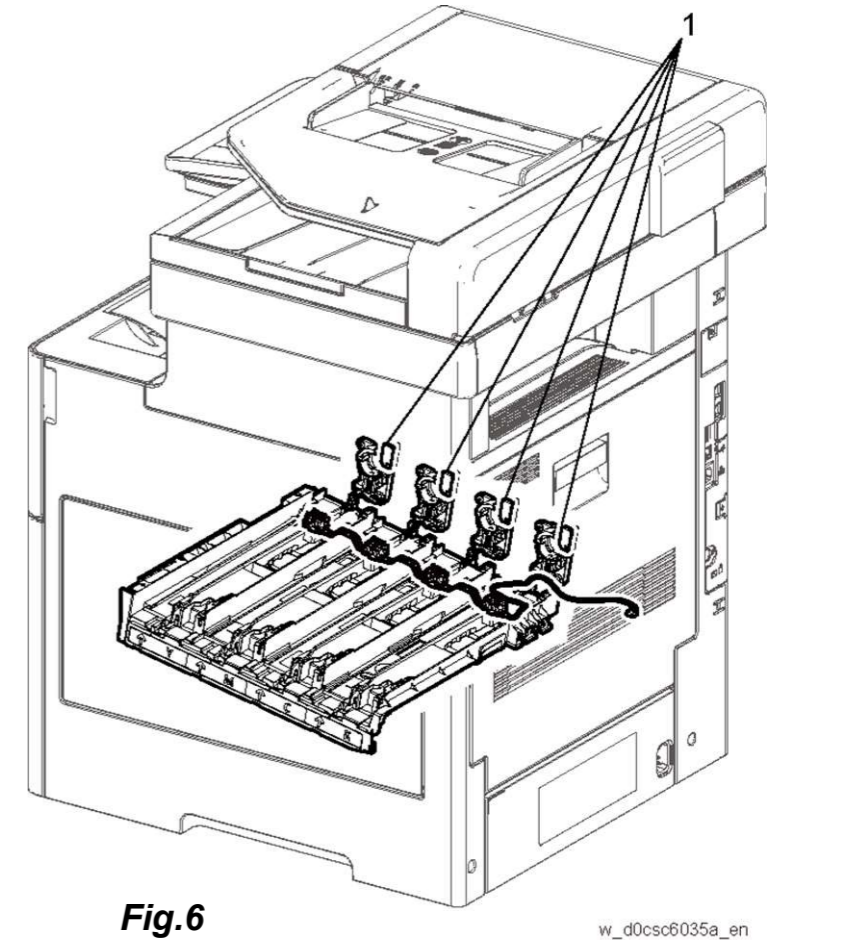
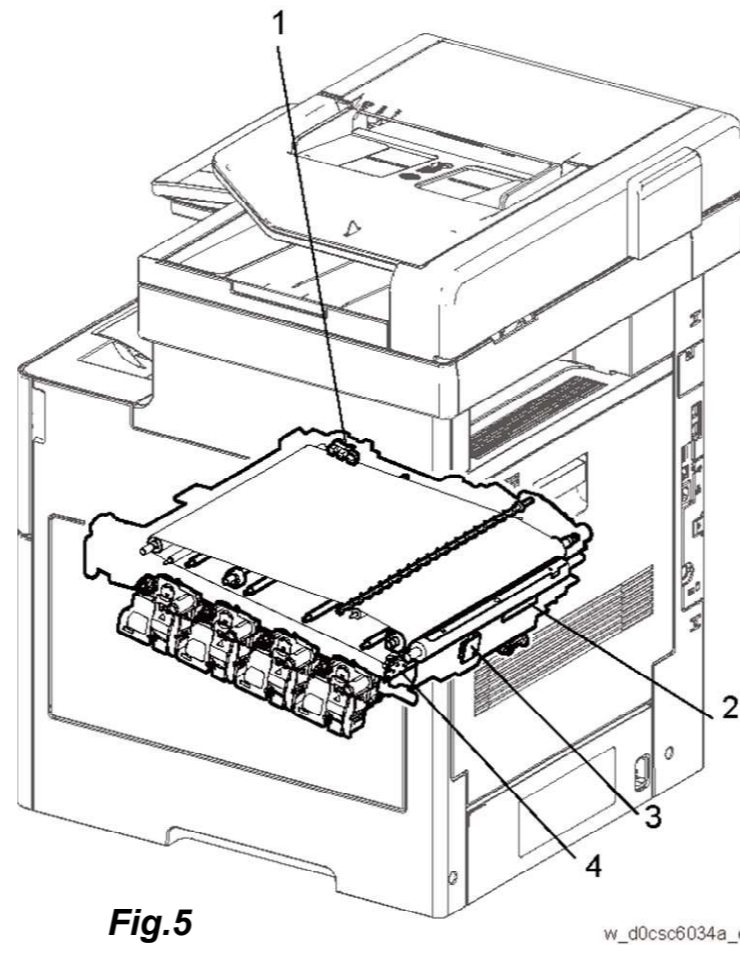
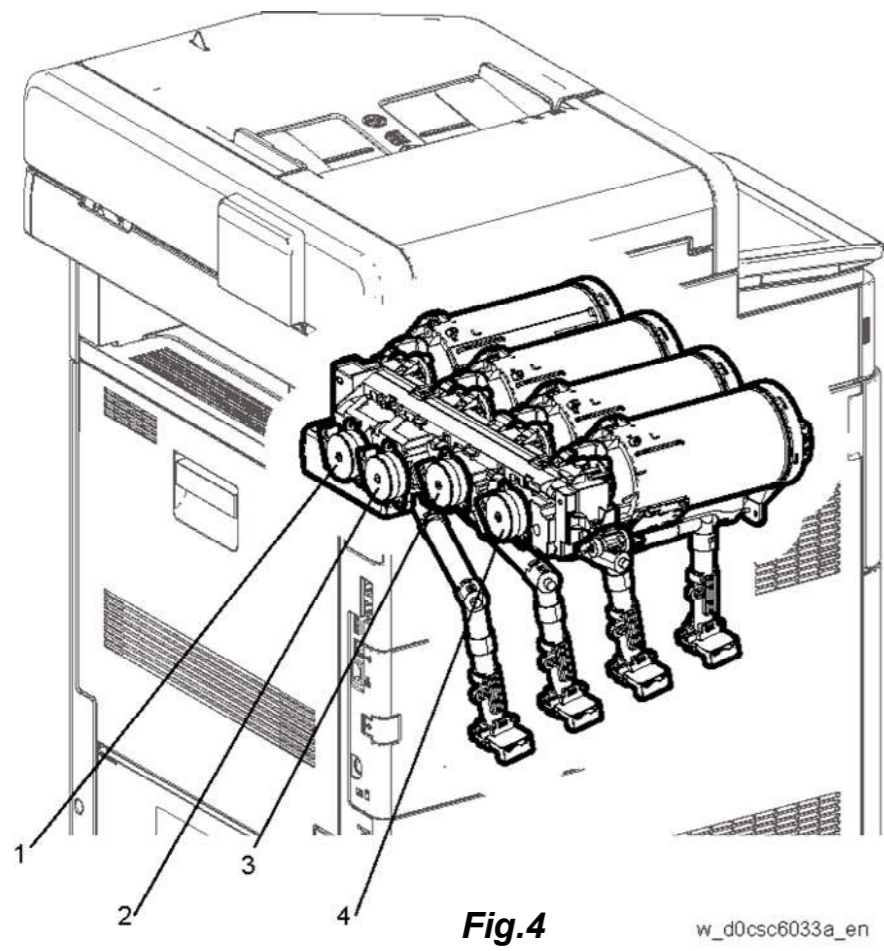
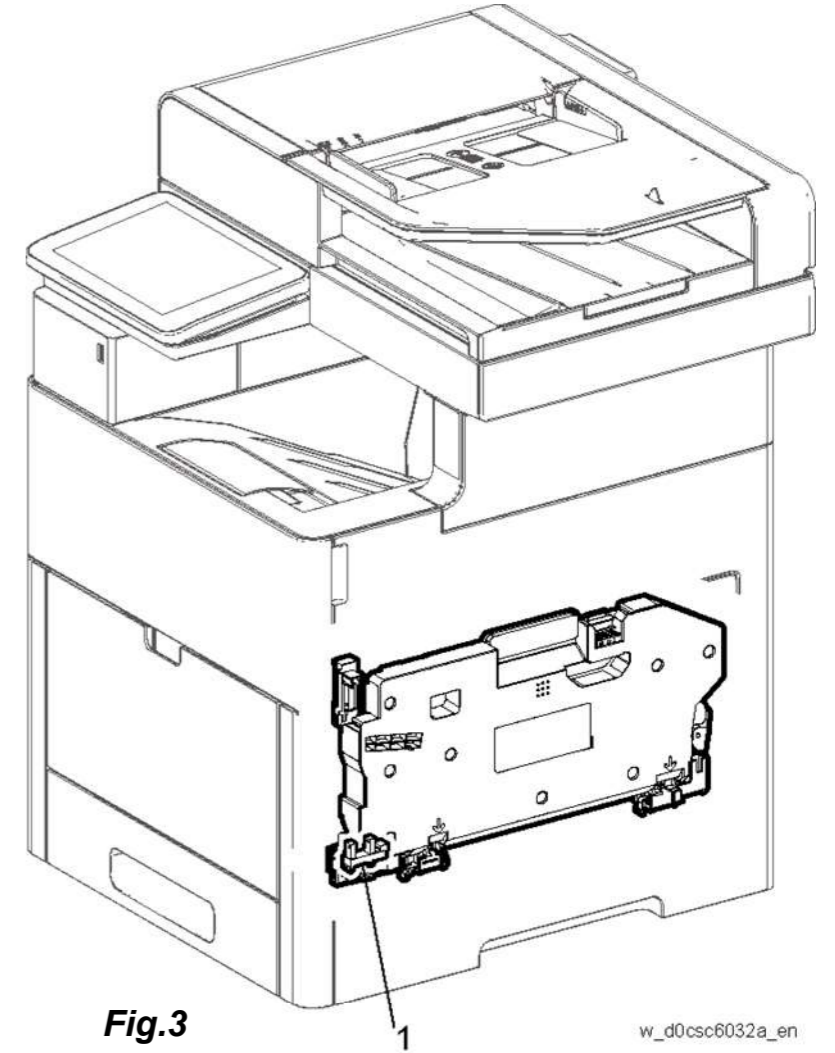
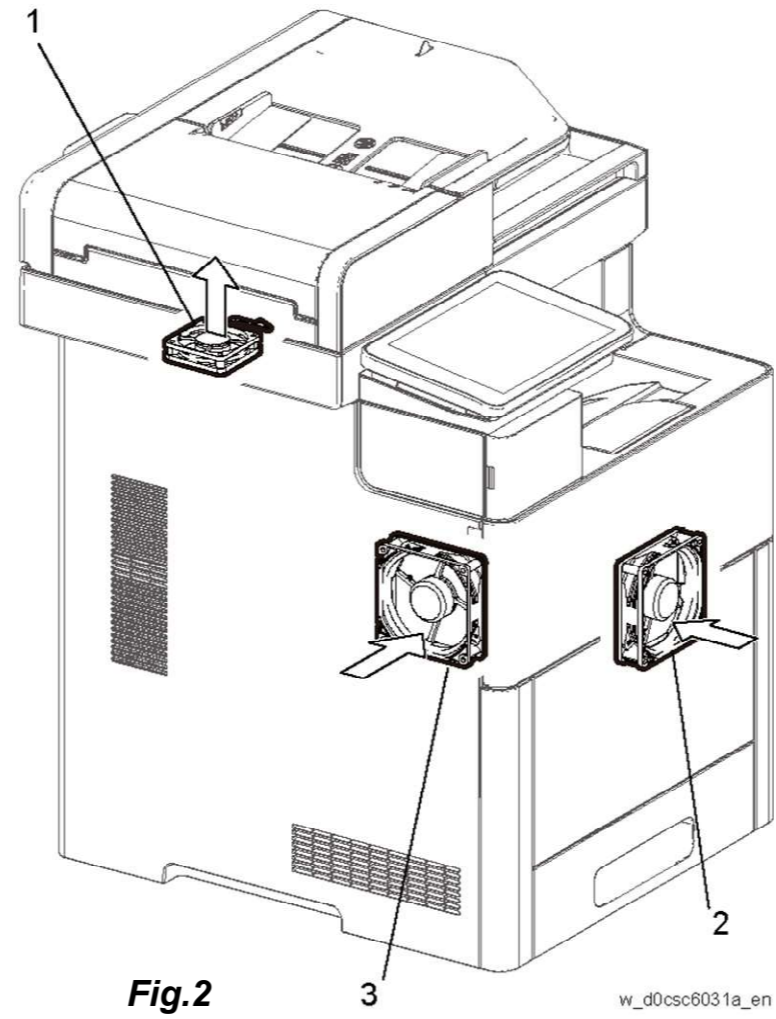
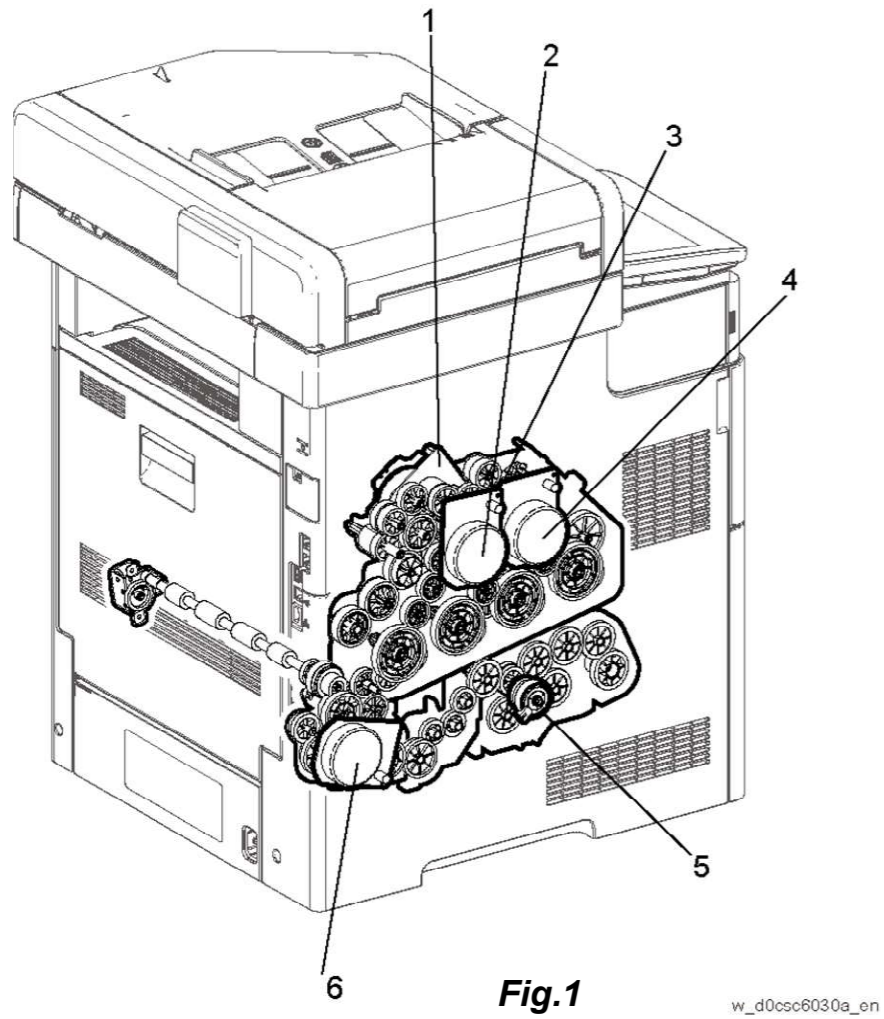


IM C530FB/C530F Point to Point Diagram





IM C530FB/C530F Point to Point Diagram Scanner



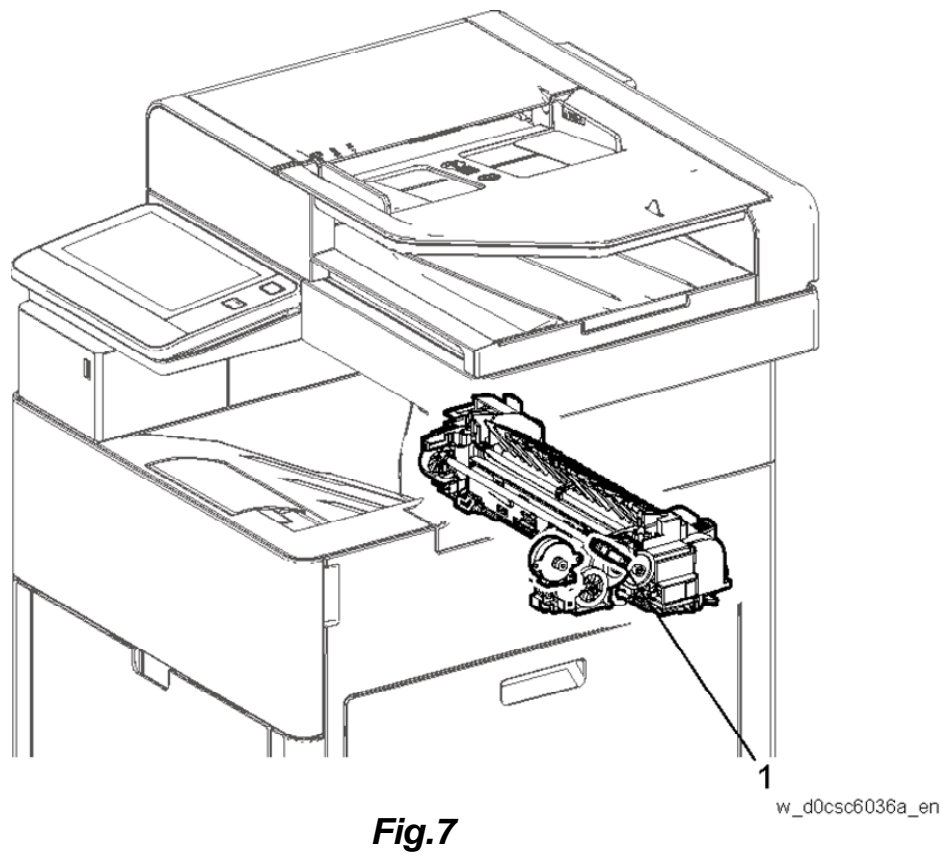


Fig. 7

w_d0csc6036a_en

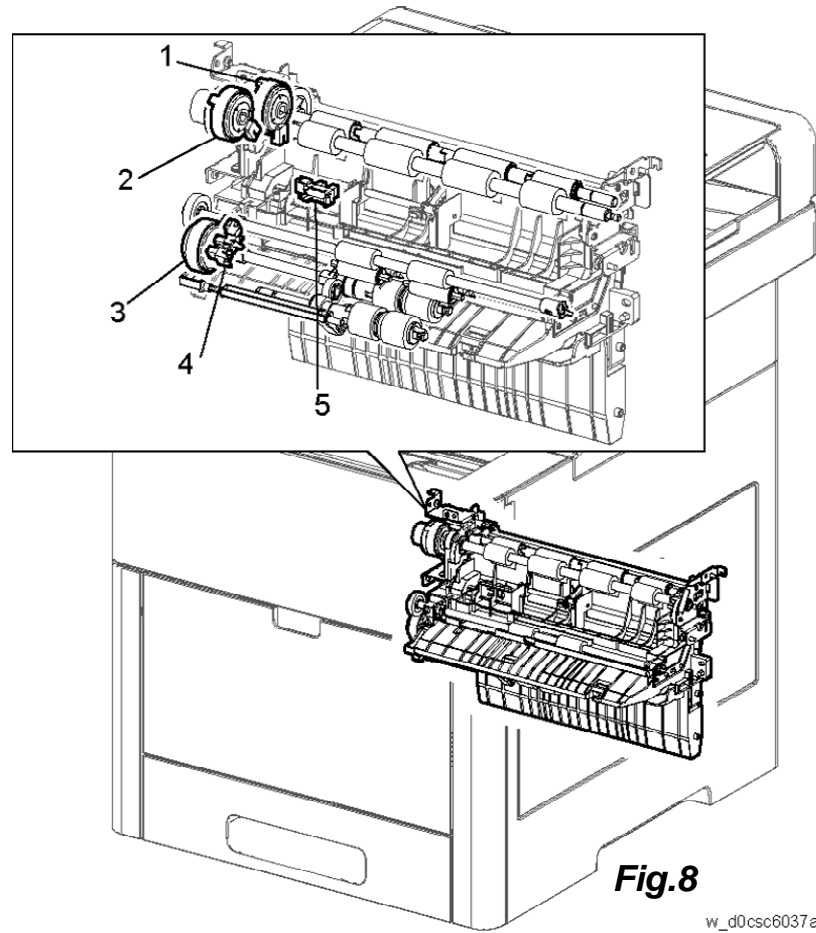


Fig. 8

w_d0csc6037a_en

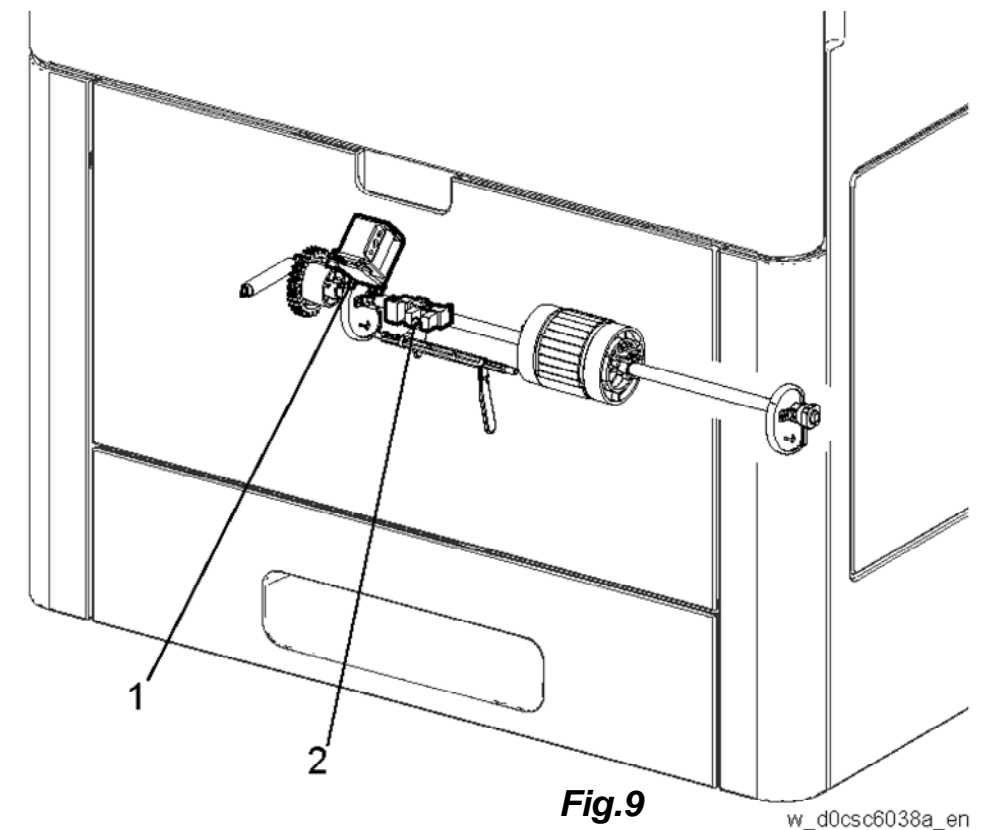


Fig. 9

w_d0csc6038a_en

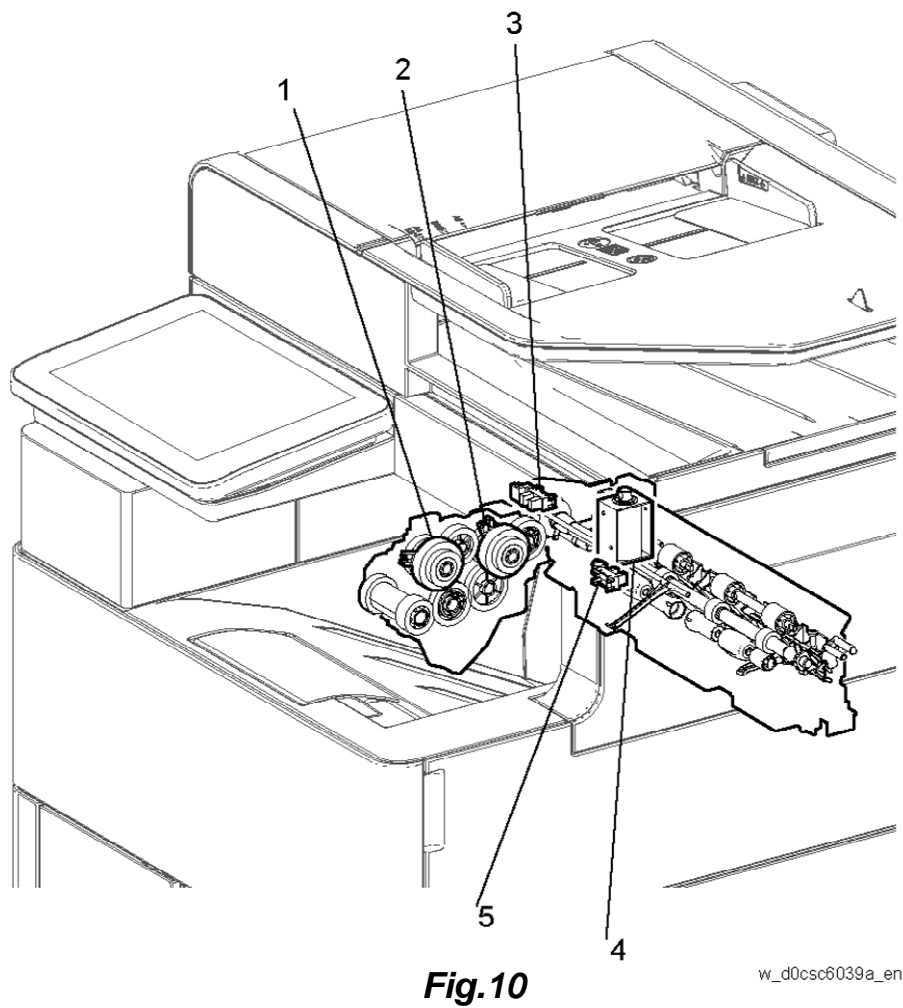


Fig. 10

w_d0csc6039a_en

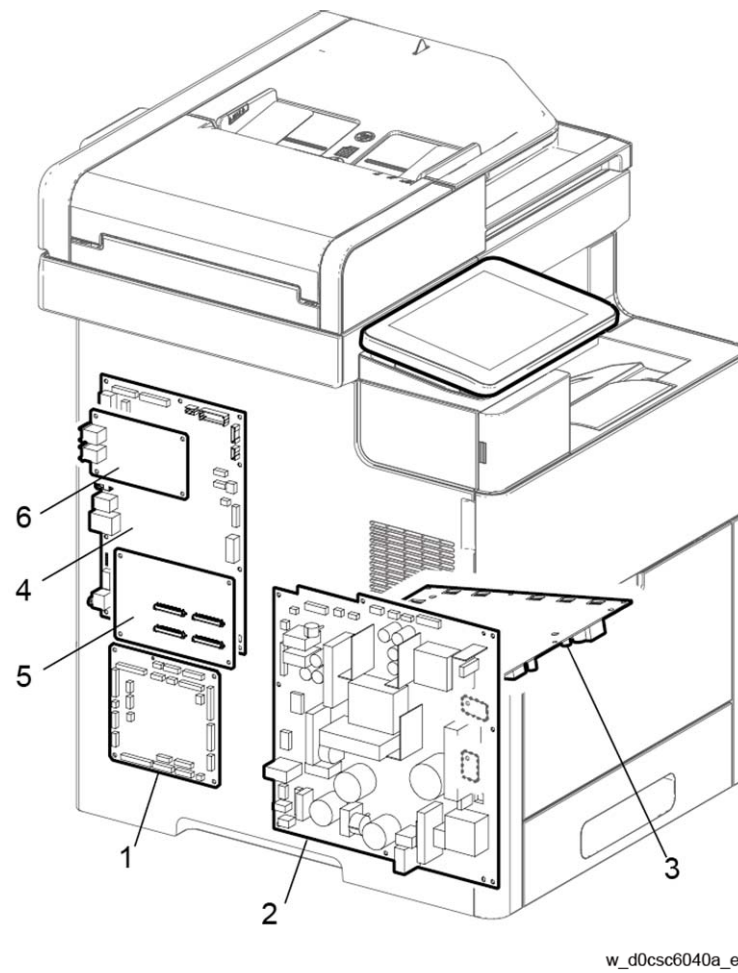


Fig. 11

w_d0csc6040a_en

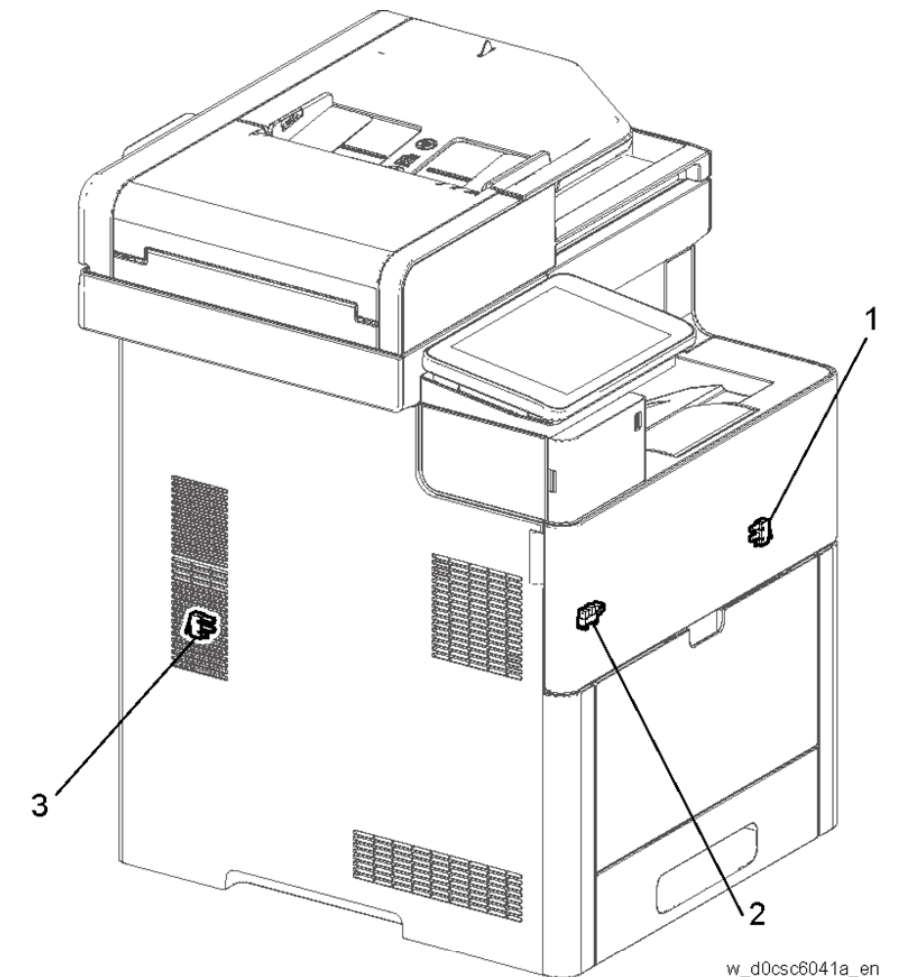


Fig. 12

w_d0csc6041a_en

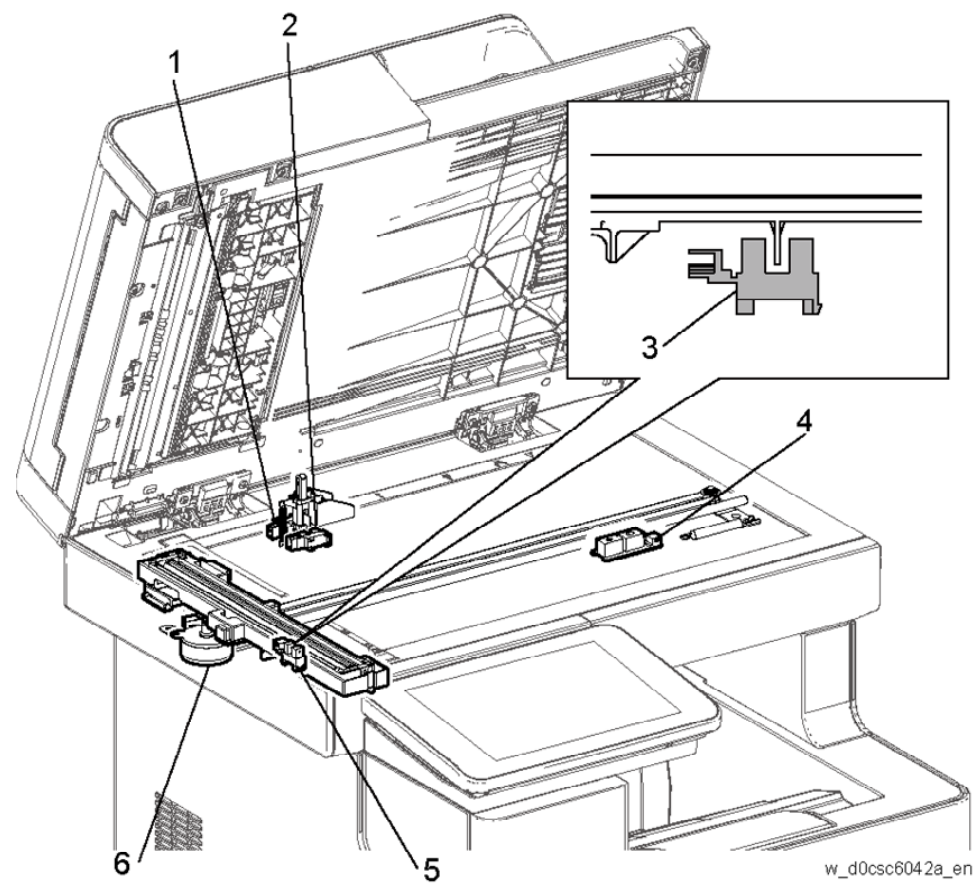


Fig.13

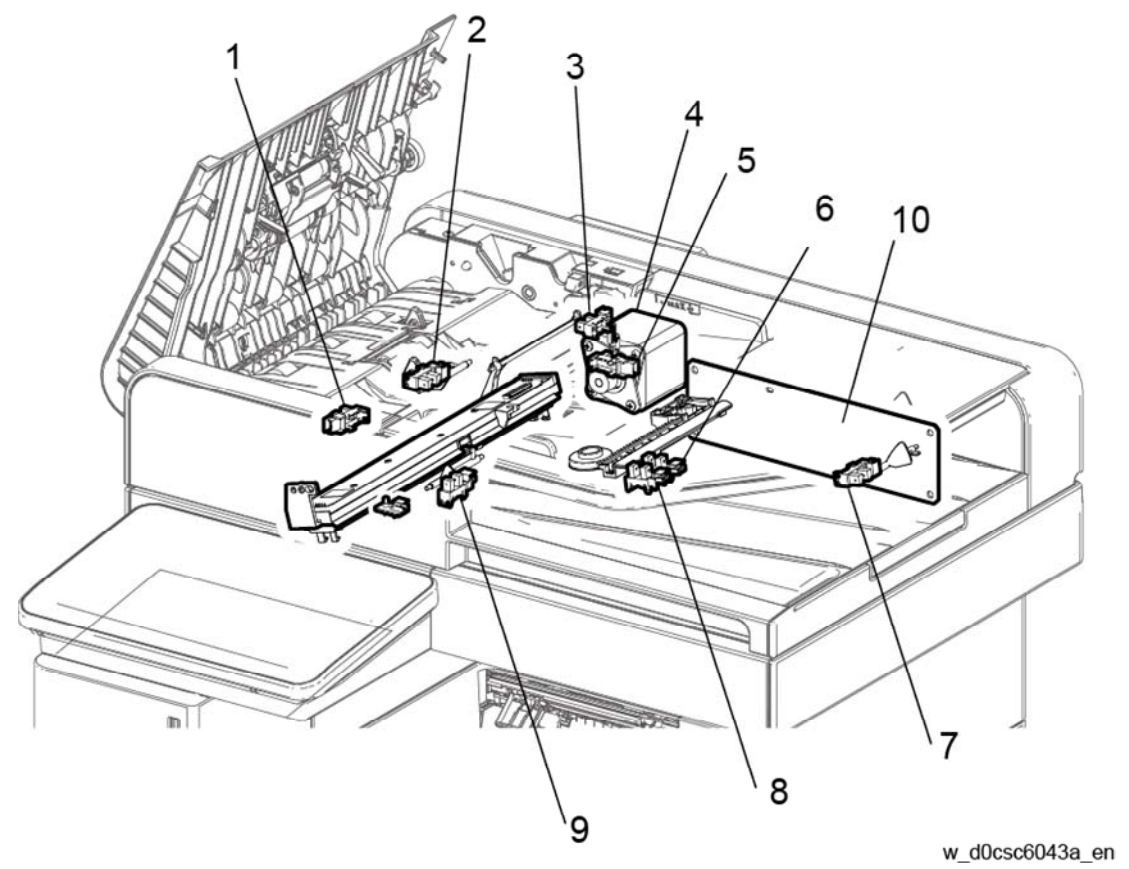


Fig.14

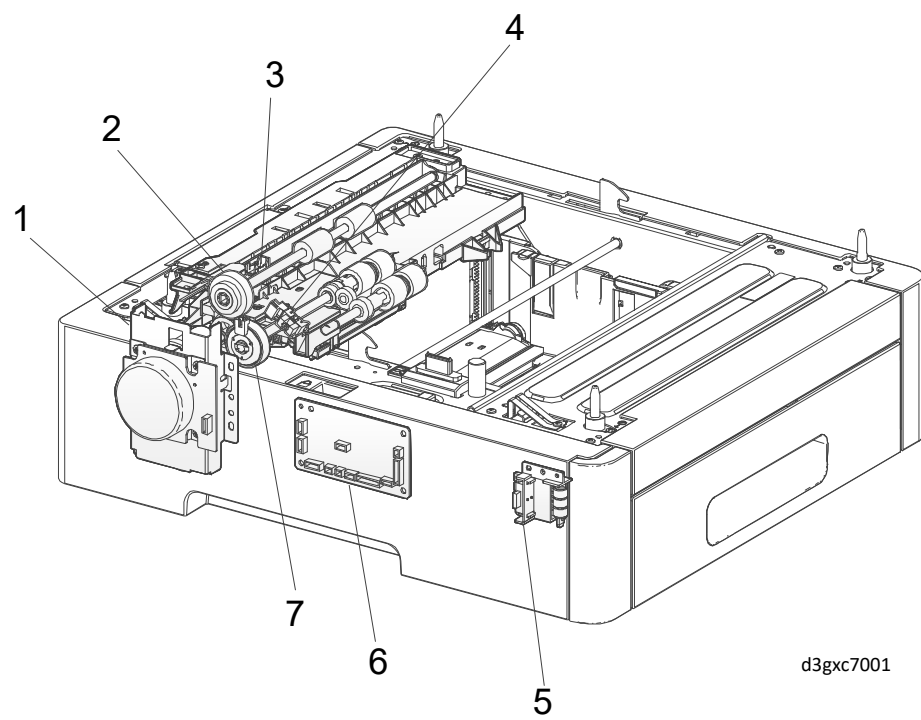


Fig.15

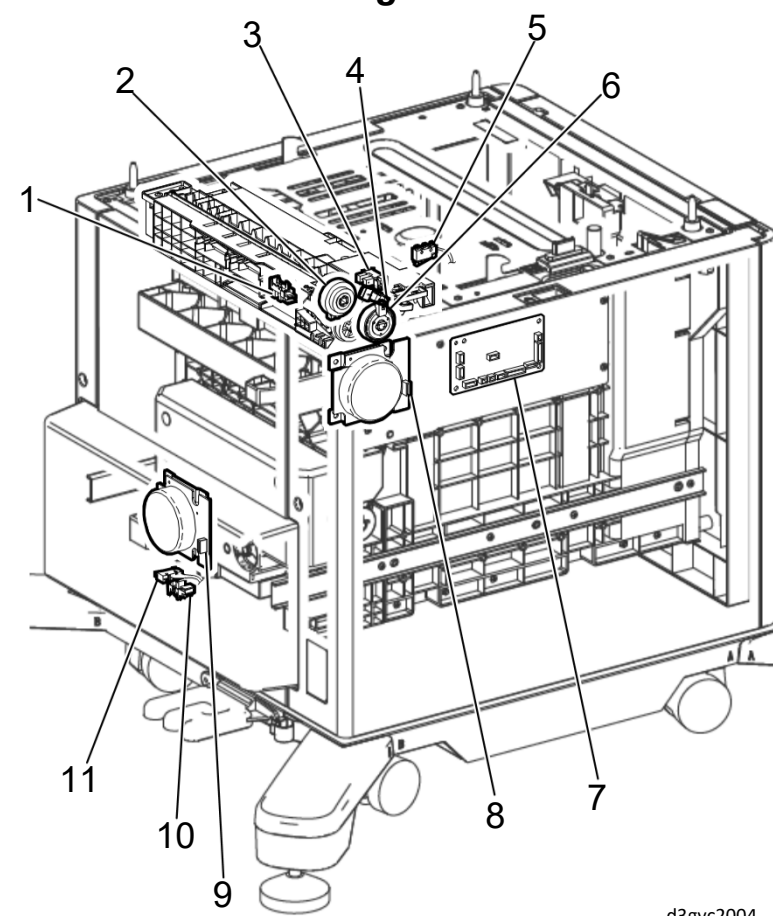


Fig.16

Symbol	Index No.	Description
Motors		
M1	Fig.1-6	Paper Feed/K Development Motor
M2	Fig.1-1	Main Motor
M3	Fig.1-4	Sub Motor
M4	Fig.1-2	Color Development Motor
M5	Fig.7-2	Fusing Envelope Motor
M6	Fig.4-3	Toner Supply Motor (M)
M7	Fig.4-1	Toner Supply Motor (K)
M8	Fig.4-4	Toner Supply Motor (Y)
M9	Fig.4-2	Toner Supply Motor (C)
M10	Fig.13-6	Scanner Motor
M11	Fig.14-4	SPDF Motor
M30	Fig.15-1	PFU Feed Motor
M31	Fig.15-2	PFU Transport Clutch
M40	Fig.16-8	LCT Feed Motor
M41	Fig.16-9	Lift Motor
Sensors		
S1	Fig.8-5	Registration Sensor
S2	Fig.8-4	Paper End Sensor
S3	Fig.5-1	K Mode Sensor
S4	Fig.10-3	Full Stuck Sensor
S5	Fig.10-5	Exit Sensor
S6	Fig.9-2	Bypass Paper End Sensor
S7	Fig.3-1	Toner Full Sensor
S8	Fig.12-2	Toner Cover Sensor
S9	Fig.7-1	Envelope Mode Sensor
S10	Fig.5-2	ID/MUSIC Sensor
S11	Fig.5-4	MUSIC Sensor
S12	Fig.5-3	Humidity and Temperature Sensor
S13	Fig.13-3, 5	Scanner Home Position Sensor
S14	Fig.13-1	Platen Angle Sensor 1
S15	Fig.13-2	Platen Angle Sensor 2
S16	Fig.13-4	APS Sensor
S17	Fig.14-3	SPDF Document Sensor
S18	Fig.14-5	SPDF Cover Sensor
S19	Fig.14-2	SPDF Feed Sensor
S20	Fig.14-1	SPDF Registration Sensor
S21	Fig.14-9	SPDF Exit Sensor
S22	Fig.14-6	Document Tray Set Guide Sensor 1
S23	Fig.14-8	Document Tray Set Guide Sensor 2
S24	Fig.14-7	SPDF APS Sensor
S30	Fig.15-4	PFU Paper End Sensor
S31	Fig.15-3	PFU Transport Sensor
S40	Fig.16-4	LCT Paper End Sensor
S41	Fig.16-1	LCT Transport Sensor
S42	Fig.16-3	Lift Up Sensor
S43	Fig.16-10	Tray Open Sensor

Symbol	Index No.	Description
Fans		
FAN1	Fig.2-3	Main Fan
FAN2	Fig.2-2	Sub Fan
FAN3	Fig.2-1	Rear Fan
Clutches		
CL1	Fig.8-2	Registration Clutch
CL2	Fig.8-1	Duplex Clutch
CL3	Fig.8-3	Paper Feed Clutch
CL4	Fig.10-1	Invert Clutch
CL5	Fig.10-2	Exit Clutch
CL6	Fig.1-3	K Mode Clutch
CL7	Fig.1-5	Bypass Feed Clutch
CL8	-	SPDF Feed Clutch
CL9	-	SPDF Transport Clutch
CL30	Fig.15-7	PFU Feed Clutch
CL40	Fig.16-6	LCT Feed Clutch
CL41	Fig.16-2	LCT Transport Clutch
Solenoids		
SOL1	Fig.10-4	Exit Gate Solenoid
SOL2	Fig.9-1	Bypass Feed Solenoid
Switches		
SW1	Fig.12-3	Rear Interlock Switch
SW2	Fig.12-1	Side Interlock Switch
SW3	-	Paper Size Switch
SW30	Fig.15-5	PFU Paper Size Switch
SW40	Fig.16-5	Tray Limit Switch
SW41	Fig.16-11	LCT Interlock Switch
PCBs		
PCB1	Fig.11-4	Controller Board
PCB2	Fig.11-1	MCU (Engine Board)
PCB3	Fig.11-5	IPU
PCB4	Fig.11-2	LVPS
PCB5	Fig.11-3	HVPS
PCB6	Fig.11-6	FCU
PCB7	Fig.14-10	SPDF Controller Board
PCB8	Fig.6-1	Erase Lamp (Y)
PCB9	Fig.6-1	Erase Lamp (M)
PCB10	Fig.6-1	Erase Lamp (C)
PCB11	Fig.6-1	Erase Lamp (K)
PCB14	Fig.11-7	Operation Panel(OPU Controller Board)
PCB30	Fig.15-6	PFU Controller Board
PCB40	Fig.16-7	LCT Controller Board

[CN200]

Pin No.	Signal Name	I/O	CN(To)	Signal Info.
1	+5V_IO	P	Key Card	+5V_IO
2	KEY_CD_SET_N	I	Key Card	Key Card : Copy
3	KEYCD_B0	O	Key Card	Key Card : Data b 0 (Size)
4	KEYCD_B1	O	Key Card	Key Card : Data b 1 (Size)
5	KEYCD_B2	O	Key Card	Key Card : Data b 2 (Size)
6	KEYCD_B3	O	Key Card	Key Card : Data b 3 (Size)
7	KEYCD_B4	O	Key Card	Key Card : Data b 4 (Mode)
8	KEYCD_B5	O	Key Card	Key Card : Data b 5 (Mode)
9	KEYCD_B6	O	Key Card	Key Card : Data b 6 (Duplex)
10	KEYCD_B7	O	Key Card	Key Card : Data b 7 (Motor)
11	GND	G	Key Card	GND
12	KEYCD_ON	O	Key Card	Key Card : Count
13	+24VCNT	P	Key Card	+24VCNT

[CN101]

Pin No.	Signal Name	I/O	CN(To)	Signal Info.
1	+24V	P	PSU	24V ESS
2	GND	P	PSU	
3	GND	P	PSU	
4	5VX	P	PSU	5V ESS

[CN105]

Pin No.	Signal Name	I/O	CN(To)	Signal Info.
1	ACSW_START_ON_N	I	DC SW	Push DC SW and Lo Input
2	GND	G	DC SW	

[CN400]

Pin No.	Signal Name	I/O	CN(To)	Signal Info.
1	GND	G	CIS (FB)	GND
2	LVDS4-U	I	CIS (FB)	LVDS Data Output Signal
3	LVDS4+U	I	CIS (FB)	LVDS Data Output Signal
4	GND	G	CIS (FB)	GND
5	LVDS3-U	I	CIS (FB)	LVDS Data Output Signal
6	LVDS3+U	I	CIS (FB)	LVDS Data Output Signal
7	GND	G	CIS (FB)	GND
8	LVDS_RCLK-U	I	CIS (FB)	LVDS CLK Output Signal
9	LVDS_RCLK+U	I	CIS (FB)	LVDS CLK Output Signal
10	GND	G	CIS (FB)	GND
11	LVDS2-U	I	CIS (FB)	LVDS Data Output Signal
12	LVDS2+U	I	CIS (FB)	LVDS Data Output Signal
13	GND	G	CIS (FB)	GND
14	LVDS1-U	I	CIS (FB)	LVDS Data Output Signal
15	LVDS1+U	I	CIS (FB)	LVDS Data Output Signal
16	GND	G	CIS (FB)	GND
17	LVDS0-U	I	CIS (FB)	LVDS Data Output Signal
18	LVDS0+U	I	CIS (FB)	LVDS Data Output Signal
19	GND	G	CIS (FB)	GND
20	MCLK-U_N	O	CIS (FB)	LVDS CLK Input Signal
21	MCLK+U_P	O	CIS (FB)	LVDS CLK Input Signal
22	GND	G	CIS (FB)	GND
23	CIS_SYDI_U	I	CIS (FB)	Signal for serial communication
24	CIS_SYCLK_U	O	CIS (FB)	Signal for serial communication
25	CIS_SYDO_U	O	CIS (FB)	Signal for serial communication
26	CIS_SYCS_U_N	O	CIS (FB)	Signal for serial communication
27	GND	G	CIS (FB)	GND
28	GND	G	CIS (FB)	GND
29	-	-	-	N.C (TRIG Signal : Not use)
30	GND	G	CIS (FB)	GND
31	GND	G	CIS (FB)	GND
32	GND	G	CIS (FB)	GND
33	+3.3V_CIS_U	O	CIS (FB)	+3.3V
34	+3.3V_CIS_U	O	CIS (FB)	+3.3V
35	+3.3V_CIS_U	O	CIS (FB)	+3.3V
36	+3.3V_CLED_U	O	CIS (FB)	+3.3V
37	+3.3V_CLED_U	O	CIS (FB)	+3.3V
38	+3.3V_CLED_U	O	CIS (FB)	+3.3V
39	GND	G	CIS (FB)	GND
40	GND	G	CIS (FB)	GND

[CN401]

Pin No.	Signal Name	I/O	CN(To)	Signal Info.
1	GND	G	CIS (DF)	GND
2	LVDS4-D	I	CIS (DF)	LVDS Data Output Signal
3	LVDS4+D	I	CIS (DF)	LVDS Data Output Signal
4	GND	G	CIS (DF)	GND
5	LVDS3-D	I	CIS (DF)	LVDS Data Output Signal
6	LVDS3+D	I	CIS (DF)	LVDS Data Output Signal
7	GND	G	CIS (DF)	GND
8	LVDS_RCLK-D	I	CIS (DF)	LVDS CLK Output Signal
9	LVDS_RCLK+D	I	CIS (DF)	LVDS CLK Output Signal
10	GND	G	CIS (DF)	GND
11	LVDS2-D	I	CIS (DF)	LVDS Data Output Signal
12	LVDS2+D	I	CIS (DF)	LVDS Data Output Signal
13	GND	G	CIS (DF)	GND
14	LVDS1-D	I	CIS (DF)	LVDS Data Output Signal
15	LVDS1+D	I	CIS (DF)	LVDS Data Output Signal
16	GND	G	CIS (DF)	GND
17	LVDS0-D	I	CIS (DF)	LVDS Data Output Signal
18	LVDS0+D	I	CIS (DF)	LVDS Data Output Signal
19	GND	G	CIS (DF)	GND
20	MCLK-D_N	O	CIS (DF)	LVDS CLK Input Signal
21	MCLK+D_P	O	CIS (DF)	LVDS CLK Input Signal
22	GND	G	CIS (DF)	GND
23	CIS_SYDI_D	I	CIS (DF)	Signal for serial communication
24	CIS_SYCLK_D	O	CIS (DF)	Signal for serial communication
25	CIS_SYDO_D	O	CIS (DF)	Signal for serial communication
26	CIS_SYCS_D_N	O	CIS (DF)	Signal for serial communication
27	GND	G	CIS (DF)	GND
28	GND	G	CIS (DF)	GND
29	-	-	-	N.C (TRIG Signal : Not use)
30	GND	G	CIS (DF)	GND
31	GND	G	CIS (DF)	GND
32	GND	G	CIS (DF)	GND
33	+3.3V_CIS_D	O	CIS (DF)	+3.3V
34	+3.3V_CIS_D	O	CIS (DF)	+3.3V
35	+3.3V_CIS_D	O	CIS (DF)	+3.3V
36	+3.3V_CLED_D	O	CIS (DF)	+3.3V
37	+3.3V_CLED_D	O	CIS (DF)	+3.3V
38	+3.3V_CLED_D	O	CIS (DF)	+3.3V
39	GND	G	CIS (DF)	GND
40	GND	G	CIS (DF)	GND

[CN403]

Pin No.	Signal Name	I/O	CN(To)	Signal Info.
1	A	I/O	FB MOTOR	A phase
2	A/	I/O	FB MOTOR	A/ phase
3	B/	I/O	FB MOTOR	B/ phase
4	B	I/O	FB MOTOR	B phase

[CN404]

Pin No.	Signal Name	I/O	CN(To)	Signal Info.
1	NC	-	-	-
2	NC	-	-	-
3	NC	-	-	-
4	GND	G	FB APS	GND
5	APS1_N	I	FB APS	APS Sensor
6	+5V_APS	P	FB APS	+5V

[CN405] HPS/AKS/LUS I/F

Pin No.	Signal Name	I/O	CN(To)	Signal Info.
1	+5V_SCN1	P	FB LUS	+5V
2	GND	G	FB LUS	GND
3	PLATEN_N (LUS)	I	FB LUS	Platen Angle Sensor
4	+5V_SCN	P	FB HPS	+5V
5	GND	G	FB HPS	GND
6	REGI (HPS)	I	FB HPS	Home Position Sensor
7	+5VE_Z	P	FB AKS	Power supply for energy saving
8	GND	G	FB AKS	GND
9	ANGLE (AKS)	I	FB AKS	Platen Angle Sensor

[CN406] DF I/F

Pin No.	Signal Name	I/O	CN(To)	Signal Info.
A1	+24V_SCN	P	DF	+24V
A2	+24V_SCN	P	DF	+24V
A3	GND	G	DF	GND
A4	DF_ROM_SYCS_N	O	DF	EEPROM SCIF
A5	DF_ROM_SYDO	O	DF	EEPROM SCIF
A6	GND	G	DF	GND
A7	DFSET_N	I	DF	ADF detection
A8	+5VE_Z	P	DF	Power supply for energy saving
A9	DFRST_N	O	DF	ADF reset
A10	DOC_N	I	DF	Document detection(DOC)
A11	CSYNC_N	I	DF	sscan
A12	DFSTART	O	DF	ADF start
A13	STS_P	I	DF	UART(RX_P)
A14	GND	G	DF	GND
A15	GND	G	DF	GND
B15	+24V_SCN	P	DF	+24V
B14	+24V_SCN	P	DF	+24V
B13	GND	G	DF	GND
B12	DF_ROM_SYDI	I	DF	EEPROM SCIF
B11	DF_ROM_SYCLK	O	DF	EEPROM SCIF
B10	+3.3V_SCN	P	DF	+3.3V
B9	GND	G	DF	GND
B8	+5V_SCN	P	DF	+5V
B7	+5V_SCN	P	DF	+5V
B6	PSYNC_N	I	DF	sscan
B5	DOC_RDY	I	DF	ADF start
B4	DF_OPEN_DET	O	DF	ADF open/close(LUS input)
B3	STS_N	I	DF	UART(RX_N)
B2	CMD_N	O	DF	UART(TX_N)
B1	CMD_P	O	DF	UART(TX_P)

IPU I/F

Pin No.	Signal Name	I/O	CN(To)	Signal Info.
1	N.C.	N	IPU	Connect to Spare GND
2	GND	G	IPU	
3	FV_IPDAT20	O	IPU	Binary image data_M (0:White,1:Black)
4	FV_IPDAT21	O	IPU	Binary image data_M (0:White,1:Black)
5	GND	G	IPU	
6	FV_IPDAT22	O	IPU	Binary image data_M (0:White,1:Black)
7	FV_IPDAT23	O	IPU	Binary image data_M (0:White,1:Black)
8	GND	G	IPU	
9	FV_IPDAT24	O	IPU	Binary image data_M (0:White,1:Black)
10	FV_IPDAT25	O	IPU	Binary image data_M (0:White,1:Black)
11	GND	G	IPU	
12	FV_IPDAT26	O	IPU	Binary image data_M (0:White,1:Black)
13	FV_IPDAT27	O	IPU	Binary image data_M (0:White,1:Black)
14	GND	G	IPU	
15	FV_MLSYNC2_N	I	IPU	Main scan image synchronization (Low Active)
16	FV_MFSYNC2_N	I	IPU	Sub scan image synchronization (Low Active)
17	GND	G	IPU	
18	FV_IPLGATE2_N	O	IPU	Video Data_term of validity (Low Active)
19	GND	G	IPU	
20	FV_IPDAT30	O	IPU	Binary image data_Y (0:White,1:Black)
21	FV_IPDAT31	O	IPU	Binary image data_Y (0:White,1:Black)
22	GND	G	IPU	
23	FV_IPDAT32	O	IPU	Binary image data_Y (0:White,1:Black)
24	FV_IPDAT33	O	IPU	Binary image data_Y (0:White,1:Black)
25	GND	G	IPU	
26	FV_IPDAT34	O	IPU	Binary image data_Y (0:White,1:Black)
27	FV_IPDAT35	O	IPU	Binary image data_Y (0:White,1:Black)
28	GND	G	IPU	
29	FV_IPDAT36	O	IPU	Binary image data_Y (0:White,1:Black)
30	FV_IPDAT37	O	IPU	Binary image data_Y (0:White,1:Black)
31	GND	G	IPU	
32	FV_IPLGATE3_N	O	IPU	Video Data_term of validity (Low Active)
33	GND	G	IPU	
34	FV_MFSYNC3_N	I	IPU	Sub scan image synchronization (Low Active)
35	FV_MLSYNC3_N	I	IPU	Main scan image synchronization (Low Active)
36	GND	G	IPU	
37	SLP_M_RECOV_ON_	O	IPU	24V ON/OFF
38	IOT_PW_ON_PSU	O	IPU	Engine Power ON/OFF Signal
39	GND	G	IPU	
40	3.3V_IN	I	IPU	Output Enable Signal

Pin No.	Signal Name	I/O	CN(To)	Signal Info.
41	N.C.	N	IPU	Connect to Spare GND
42	FV_IPDAT17	O	IPU	Binary image data_C (0:White,1:Black)
43	FV_IPDAT16	O	IPU	Binary image data_C (0:White,1:Black)
44	GND	G	IPU	
45	FV_IPDAT15	O	IPU	Binary image data_C (0:White,1:Black)
46	FV_IPDAT14	O	IPU	Binary image data_C (0:White,1:Black)
47	GND	G	IPU	
48	FV_IPDAT13	O	IPU	Binary image data_C (0:White,1:Black)
49	FV_IPDAT12	O	IPU	Binary image data_C (0:White,1:Black)
50	GND	G	IPU	
51	FV_IPDAT11	O	IPU	Binary image data_C (0:White,1:Black)
52	FV_IPDAT10	O	IPU	Binary image data_C (0:White,1:Black)
53	GND	G	IPU	
54	FV_IPLGATE1_N	O	IPU	Valid period of Video Data (Low Active)
55	GND	G	IPU	
56	FV_MLSYNC1_N	I	IPU	Main scan image synchronization (Low Active)
57	FV_MFSYNC1_N	I	IPU	Sub scan image synchronization (Low Active)
58	GND	G	IPU	
59	FV_CLK_IP	O	IPU	Video Data / Valid Synchronous clock
60	GND	G	IPU	
61	FV_MLSYNC0_N	I	IPU	Main scan image synchronization (Low Active)
62	FV_MFSYNC0_N	I	IPU	Sub scan image synchronization (Low Active)
63	GND	G	IPU	
64	FV_IPLGATE0_N	O	IPU	Valid period of Video Data (Low Active)
65	GND	G	IPU	
66	FV_IPDAT07	O	IPU	Binary image data_K (0:White,1:Black)
67	FV_IPDAT06	O	IPU	Binary image data_K (0:White,1:Black)
68	GND	G	IPU	
69	FV_IPDAT05	O	IPU	Binary image data_K (0:White,1:Black)
70	FV_IPDAT04	O	IPU	Binary image data_K (0:White,1:Black)
71	GND	G	IPU	
72	FV_IPDAT03	O	IPU	Binary image data_K (0:White,1:Black)
73	FV_IPDAT02	O	IPU	Binary image data_K (0:White,1:Black)
74	GND	G	IPU	
75	FV_IPDAT01	O	IPU	Binary image data_K (0:White,1:Black)
76	FV_IPDAT00	O	IPU	Binary image data_K (0:White,1:Black)
77	GND	G	IPU	
78	EW_CMDRDY	I	IPU	Command READY
79	EW_CMD	O	IPU	Communication Tx
80	EW_STS	I	IPU	Communication Rx