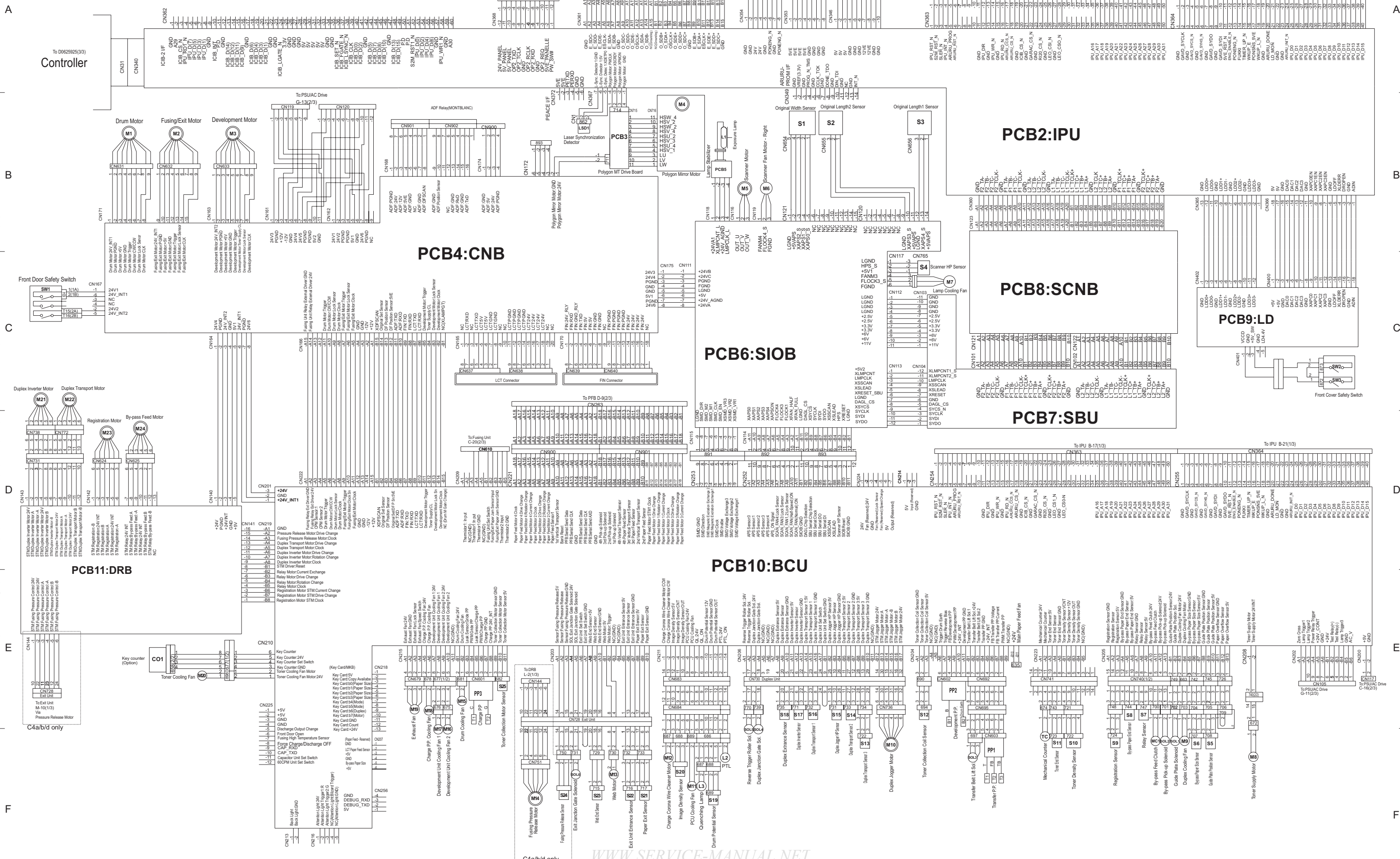


# D062/D063/D065/D066 Point To Point Diagram (1/4)





# D062/D063/D065/D066 Point To Point Diagram (3/4)

A

B

C

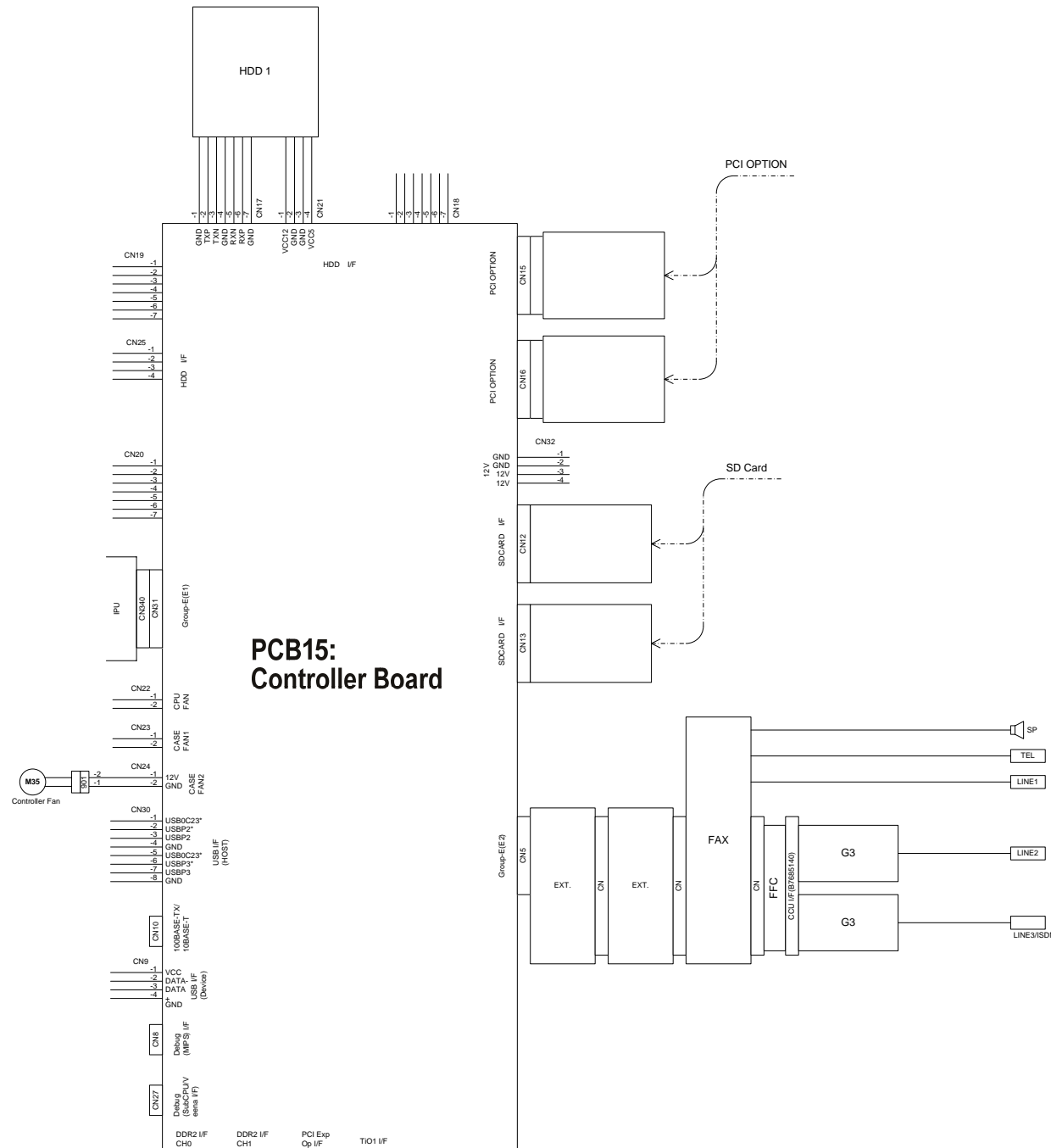
D

E

F

G

1 2 3 4 5 6 7 8 9 10



DDR-DIMM

CN2 DDR-DIMM U/F				CN2 DDR-DIMM U/F			
1	VTTREF	26	DM1	51	DQS2	76	DQ(31)
2	GND	27	GND	52	DM2	77	GND
3	GND	28	GND	53	GND	78	GND
4	DQ(4)	29	DQS1*	54	GND	79	CKE0
5	DQ(0)	30	CK0	55	DQ(18)	80	CKE1
6	DQ(5)	31	DQS1	56	DQ(22)	81	VDDQ
7	DQ(1)	32	CK0*	57	DQ(19)	82	VDDQ
8	GND	33	GND	58	DQ(23)	83	NC
9	GND	34	GND	59	GND	84	NC
10	DM0	35	DQ(10)	60	GND	85	BS2
11	DQS0*	36	DQ(14)	61	DQ(24)	86	NC
12	GND	37	DQ(11)	62	DQ(28)	87	VDDQ
13	DQS0	38	DQ(15)	63	DQ(25)	88	VDDQ
14	DQ(6)	39	GND	64	DQ(29)	89	MA(12)
15	GND	40	GND	65	GND	90	MA(11)
16	DQ(7)	41	GND	66	GND	91	MA(9)
17	DQ(2)	42	GND	67	MA(3)	92	MA(7)
18	GND	43	DQ(16)	68	DQS3*	93	MA(8)
19	DQ(3)	44	DQ(20)	69	NC	94	MA(6)
20	DQ(12)	45	DQ(17)	70	DQS3	95	VDDQ
21	GND	46	DQ(21)	71	GND	96	VDDQ
22	DQ(13)	47	GND	72	GND	97	MA(5)
23	DQ(8)	48	GND	73	DQ(26)	98	MA(4)
24	GND	49	DQS2*	74	DQ(30)	99	MA(3)
25	DQ(9)	50	NC	75	DQ(27)	100	MA(2)

DDR-DIMM

CN14 DDR-DIMM U/F				CN14 DDR-DIMM U/F			
1	VTTREF	26	DM1	51	DQS2	76	DQ(31)
2	GND	27	GND	52	DM2	77	GND
3	GND	28	GND	53	GND	78	GND
4	DQ(4)	29	DQS1*	54	GND	79	CKE0
5	DQ(0)	30	CK0	55	DQ(18)	80	CKE1
6	DQ(5)	31	DQS1	56	DQ(22)	81	VDDQ
7	DQ(1)	32	CK0*	57	DQ(19)	82	VDDQ
8	GND	33	GND	58	DQ(23)	83	NC
9	GND	34	GND	59	GND	84	NC
10	DM0	35	DQ(10)	60	GND	85	BS2
11	DQS0*	36	DQ(14)	61	DQ(24)	86	NC
12	GND	37	DQ(11)	62	DQ(28)	87	VDDQ
13	DQS0	38	DQ(15)	63	DQ(25)	88	VDDQ
14	DQ(6)	39	GND	64	DQ(29)	89	MA(12)
15	GND	40	GND	65	GND	90	MA(11)
16	DQ(7)	41	GND	66	GND	91	MA(9)
17	DQ(2)	42	GND	67	MA(3)	92	MA(7)
18	GND	43	DQ(16)	68	DQS3*	93	MA(8)
19	DQ(3)	44	DQ(20)	69	NC	94	MA(6)
20	DQ(12)	45	DQ(17)	70	DQS3	95	VDDQ
21	GND	46	DQ(21)	71	GND	96	VDDQ
22	DQ(13)	47	GND	72	GND	97	MA(5)
23	DQ(8)	48	GND	73	DQ(26)	98	MA(4)
24	GND	49	DQS2*	74	DQ(30)	99	MA(3)
25	DQ(9)	50	NC	75	DQ(27)	100	MA(2)

Group-E(E2)

CN5 Group-E(E2)				CN5 Group-E(E2)			
1	5VE	26	C/BE2*	51	AD58	76	SD_CMD_ENI
2	5VE	27	C/BE0*	52	AD56	77	GND
3	5VE	28	PCIRST	53	GND	78	SDCLK
4	5VE	29	DEVSEL*	54	AD54	79	SDCMD
5	5VE	30	IRDY*	55	AD52	80	SD_CMD
6	5VE	31	PERR*	56	AD50	81	SDWP
7	5VE	32	PAR	57	AD48	82	GND
8	5V	33	GND	58	GND	83	OP4_SCL/OP0_CLK
9	INTDI	34	AD14	59	AD46	84	OP4_CS/OP0_TXD
10	INTAL	35	AD12	60	AD44	85	OP4_SDA/OP0_RXD
11	REDI*	36	AD10	61	AD42	86	OP4_IRQ
12	GNT6*	37	AD8	62	AD40	87	OP4_ONLINE_LED_ON*
13	REQ1*	38	GND	63	GND	88	TIMER_UP
14	GNT1*	39	AD6	64	AD38	89	ENG_ENABLE*
15	GND	40	AD4	65	AD36	90	PONENGI
16	AD30	41	AD2	66	AD34	91	PONPCP*
17	AD28	42	AD0	67	AD32	92	PSAVE_FCU
18	AD26	43	GND	68	GND	93	GND
19	AD24	44	C/BE6*	69	PETXD	94	PCICLK1
20	GND	45	C/BE4*	70	SDLED/PONSENS*	95	GND
21	AD22	46	64RE*Q	71	OP2_TCLK/OP0_CLK	96	PCICLK4
22	AD20	47	64PAR	72	OP2_TXD/OP0_TXD	97	GND
23	AD18	48	GND	73	OP2_REQ	98	12V
24	AD16	49	AD62	74	RSVD	99	12V
25	GND	50	AD60	75	SD_DT_ENI	100	12V

PCI OPTION

CN15 PCI OPTION UPPER				CN16 PCI OPTION LOWER			
1	GND	26	CBE3*	51	GND	76	reserved
2	INTC*	27	GND	52	CBE1*	77	3.3VEP
3	GND	28	DBSEL2_PCI	53	AD15	78	reserved
4	reserved	29	AD23	54	AD14	79	3.3VEP
5	GND	30	AD22	55	GND	80	reserved
6	CLKRUN	31	GND	56	AD13	81	3.3VEP
7	GND	32	AD21	57	AD12	82	reserved
8	PCIRST_OPT*	33	AD20	58	AD11	83	3.3VEP
9	GND	34	AD19	59	GND	84	reserved
10	PCLK_OPT1	35	GND	60	AD10	85	3.3VEP
11	GND	36	AD18	61	AD9	86	reserved
12	GNT1*	37	AD17	62	AD8	87	3.3VEP_WSL
13	GND	38	AD16	63	GND	88	+24V
14	REDI*	39	GND	64	CBE0*	89	reserved
15	GND	40	CBE2*	65	AD7	90	reserved
16	PME*	41	FRAME*	66	AD6	91	reserved
17	AD31	42	IRDY*	67	AD5	92	reserved
18	AD30	43	GND	68	AD4	93	reserved
19	AD29	44	TRDY*	69	AD3	94	reserved
20	AD28	45	DEVSEL*	70	AD2	95	reserved
21	AD27	46	STOP*	71	3.3VEP	96	reserved
22	AD26	47	GND	72	AD1	97	reserved
23	GND	48	PERR*	73	3.3VEP	98	reserved
24	AD25	49	SERR*	74	AD0	99	reserved
25	AD24	50	PAR	75	3.3VEP	100	reserved

Group-E(E1)

CN31 Group-E(E1)				CN31 Group-E(E1)			
1	24V_IN	26	PETx2	51	ENG_ENABLE*	76	GND
2	NC	27	PETx2	52	GND	77	GND
3	GND	28	GND	53	PONSENS*	78	NC
4	GND	29	PERx2	54	ENGRDY1*	79	+5VE IN
5	RSVD	30	PERn2	55	GND	80	+5VE IN
6	WAKE*	31	GND	56	PW_SW*	81	+5VE IN
7	GND	32	PETx3	57	WKUP_LP*	82	+5VE IN
8	REFCLK1+	33	PETx3	58	WKUP_LP*	83	+5VE IN
9	REFCLK1-	34	GND	59	VDET_EPDI	84	+5VE IN
10	GND	35	PERn3	60	PONENGI*	85	+5VE IN
11	REFCLK0+	36	PERn3	61	GND	86	+5VE IN
12	REFCLK0-	37	GND	62	PONPCP*	87	+5VE IN
13	GND	38	RESET*	63	PSAVE_FCU	88	+5VE IN
14	PETx0	39	GND	64	GND	89	+5VE IN
15	PETn0	40	RSVD	65	RSVD	90	+5VE IN
16	GND	41	OP4_BUZZER(RSVD)	66	RSVD	91	+5VE IN
17	PERx0	42	GND	67	GND	92	+5VE IN
18	PERn0	43	GND	68	USB1D+(RSVD)	93	+5VE IN
19	GND	44	GND	69	USB1D-(RSVD)	94	+5VE IN
20	PETx1	45	GND	70	GND	95	+5VE IN
21	PETn1	46	GND	71	3V	96	+5VE IN
22	GND	47	PETXD	72	NC	97	+5VE IN
23	PERx1	48	PERXD	73	GND	98	+5VE IN
24	PERn1	49	GND	74	GND	99	+5VE IN
25	GND	50	TIMER_UP*	75	GND	100	+5VE IN

PCI EXPRESS OPTION

CN6 PCI EXPRESS OPTION U/F				CN6 PCI EXPRESS OPTION U/F			
A1	PRSTN1*	B1	+12V				
A2	GND	B2	+12V				
A3	+12V	B3	RSVD				
A4	GND	B4	GND				
A5	JTAG2	B5	SMCLK				
A6	JTAG3	B6	SMDAT				
A7	JTAG4	B7	GND				
A8	JTAG5	B8	+3.3V				
A9	+3.3V	B9	JTAG1				
A10	+3.3V	B10	CS0*				
A11	PWRGD	B11	WAKE*				
A12	GND	B12	RSVD				
A13	REFCLK+	B13	GND				
A14	REFCLK-	B14	HSD0(0)				
A15	GND	B15	HSD0(1)				
A16	HSD0(0)	B16	GND				
A17	HSD0(1)	B17	PRESN1?*				
A18	GND	B18	GND				

Debug(MIPS)/Serial I/F

CN2 Debug Serial I/F		CN11 TIO Option_I/F	
1	5VE	1	GND
2	GND	2	GND
3	GND	3	3.3VE_TIO
4	DBTXD	4	3.3VE_TIO
5	DBRXD	5	SPH_CS0*
6	PETXD	6	SPH_CS1*
7	PERXD	7	SPH_CS2*
8	NC	8	SPH_CS3*
		9	SPH_DIM
		10	SPH_CLK
		11	SPH_DOUT
		12	SPH_DOUT
		13	SPH_CS1*
		14	SPH_CS0*
		15	SPH_DIM
		16	SPH_CLK
		17	SPH_CS2*
		18	PD2
		19	NC
		20	RESET*
		21	SPH_CS2*
		22	INTA*
		23	I2C_CLK
		24	I2C_DATA
		25	P00
		26	PD1
		27	3.3VE_TIO
		28	3.3VE_TIO
		29	GND
		30	GND

100BASE-TX/10BASE-T

CN10 100BASE-TX/10BASE-T		CN10 100BASE-TX/10BASE-T	
1	VCC	1	VCC
2	MX0+	2	MX0+
3	MX0-	3	MX0-
4	MX1+	4	MX1+
5	MX1-	5	MX1-
6	MX2+	6	MX2+
7	MX2-	7	MX2-
8	MX3+	8	MX3+
9	MX3-	9	MX3-
10	GND	10	GND

SD Card I/F

CN12 SD Card I/F		CN13 SD Card I/F	
1	SDA1_DT(3)	1	SDA1_DT(3)
2	SDA1_CMD	2	SDA1_CMD
3	GND	3	GND
4	3.3VE_SD	4	3.3VE_SD
5	SDA_CLK	5	SDA_CLK
6	GND	6	GND
7	SDA1_DT(0)	7	SDA1_DT(0)
8	SDA1_DT(1)	8	SDA1_DT(1)
9	SDA1_DT(2)	9	SDA1_DT(2)
10	XSDA1_OD	10	XSDA1_OD
11	GND	11	GND
12	XSDA1_WP	12	XSDA1_WP

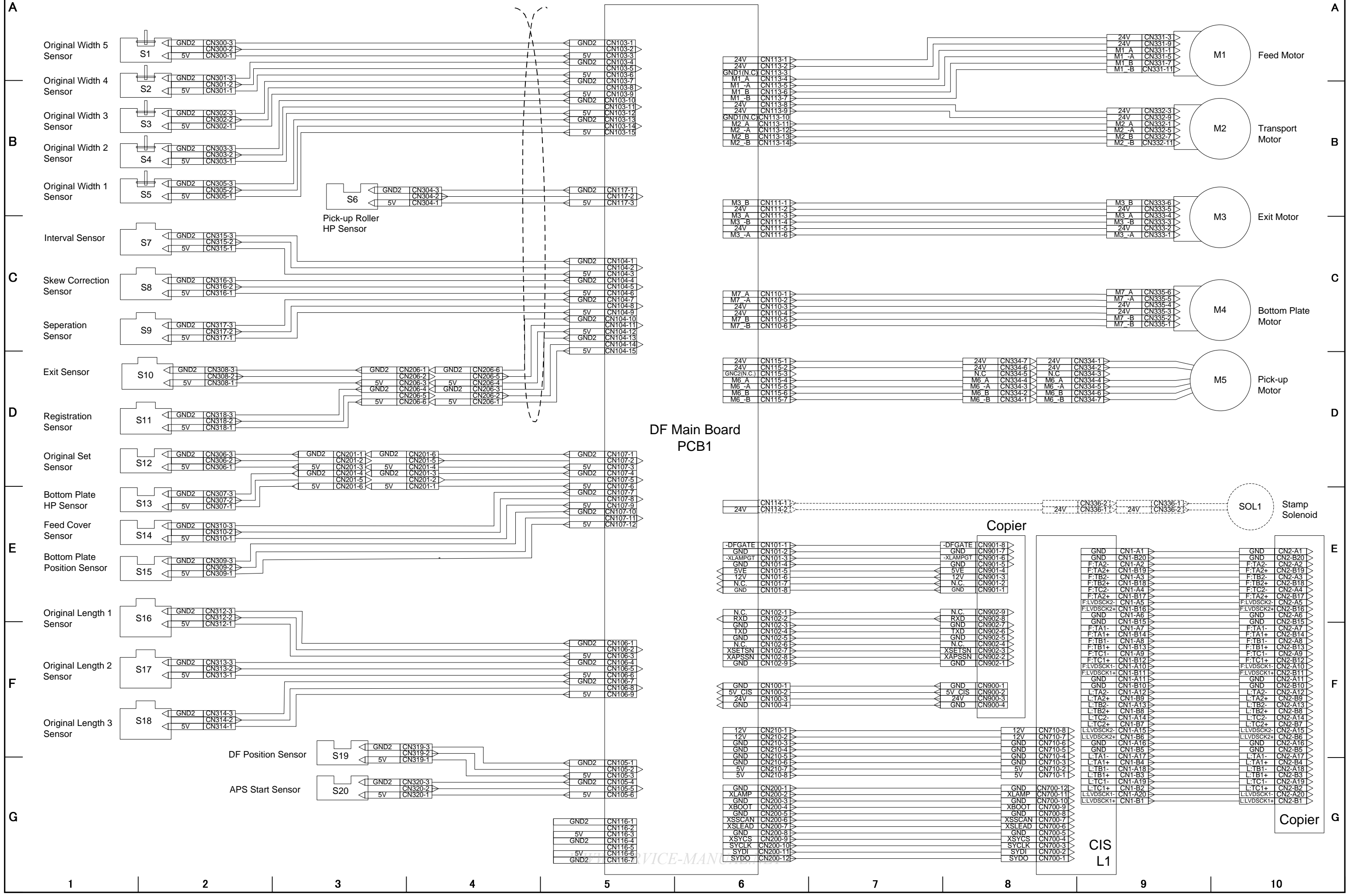
Debug(SubCPU/venal)/Serial I/F

CN27 Debug Serial I/F		CN27 Debug Serial I/F	
1	5VE	1	5VE
2	GND	2	GND
3	GND	3	GND
4	DBTXD	4	DBTXD
5	DBRXD	5	DBRXD
6	DBVTD	6	DBVTD
7	DBVRXD	7	DBVRXD
8	NC	8	NC

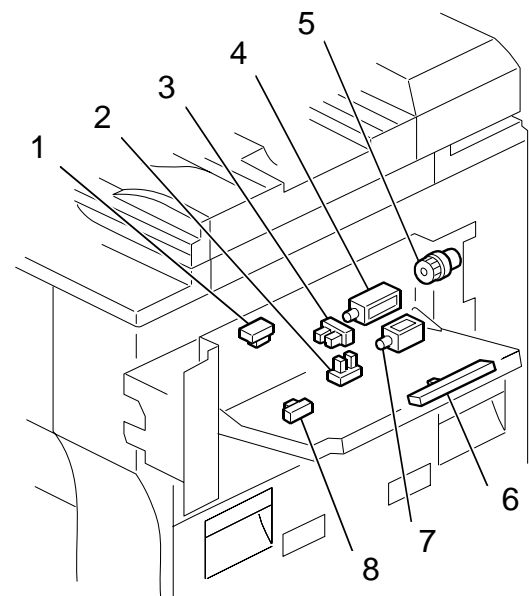
1 2 3 4 5 6 7 8 9 10



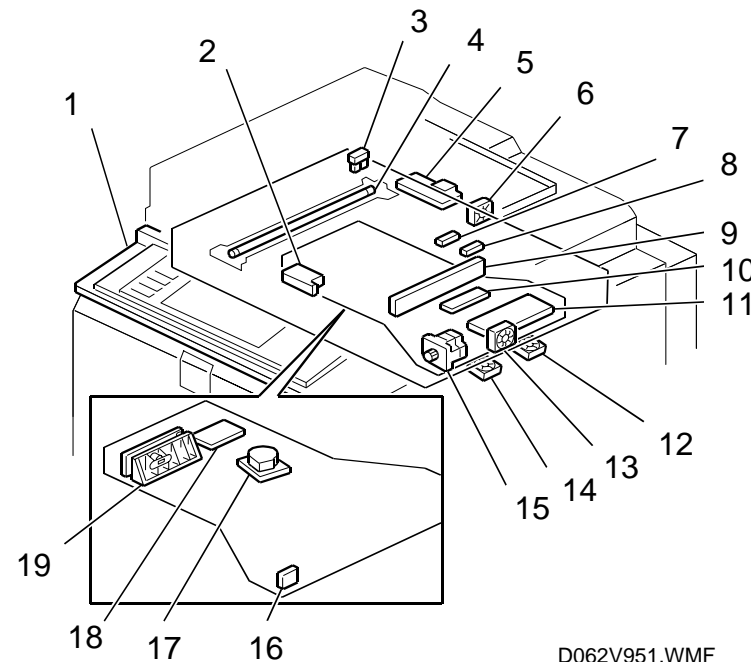
# ADF (FOR D062/D063/D065/D066) Point To Point Diagram (4/4)



# D062/D063/D065/D066 ELECTRICAL COMPONENT LAYOUT (1/4)

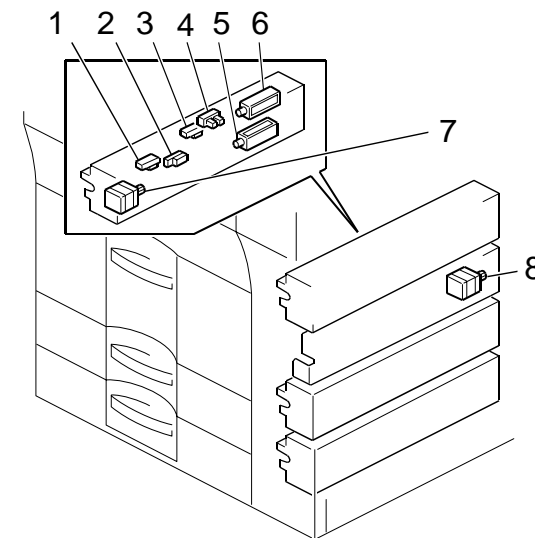


**Fig1** D062V003.WMF

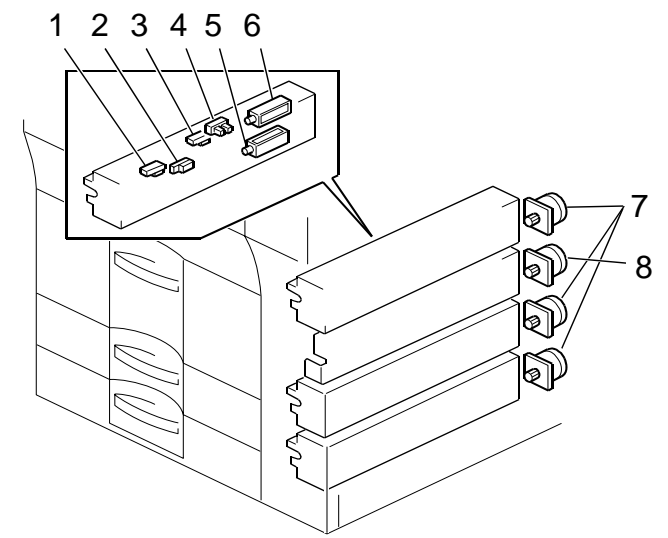


D062V951.WMF

**Fig2**

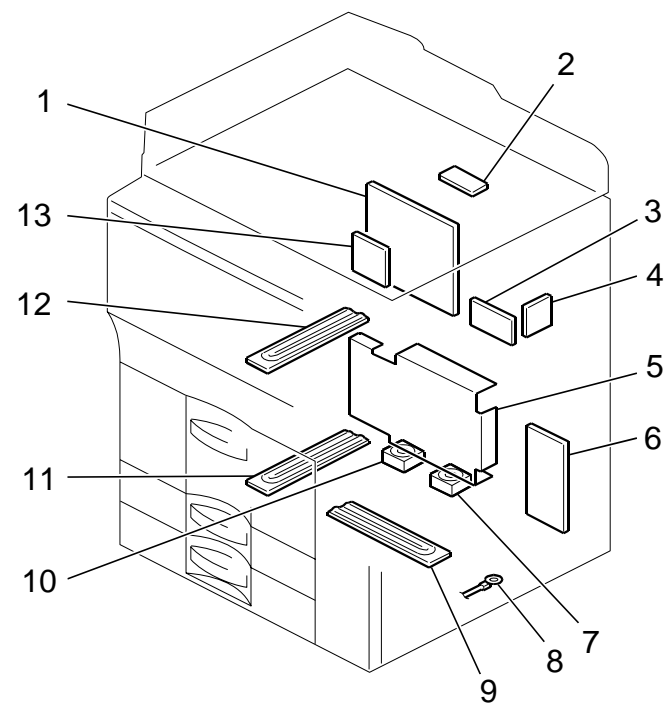


D062V954.WMF

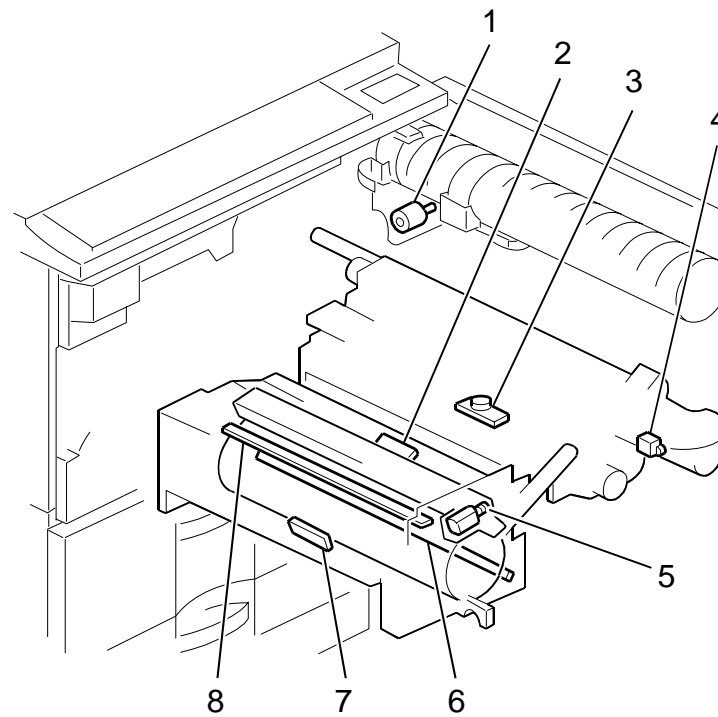


D062V954A.WMF

**Fig3**

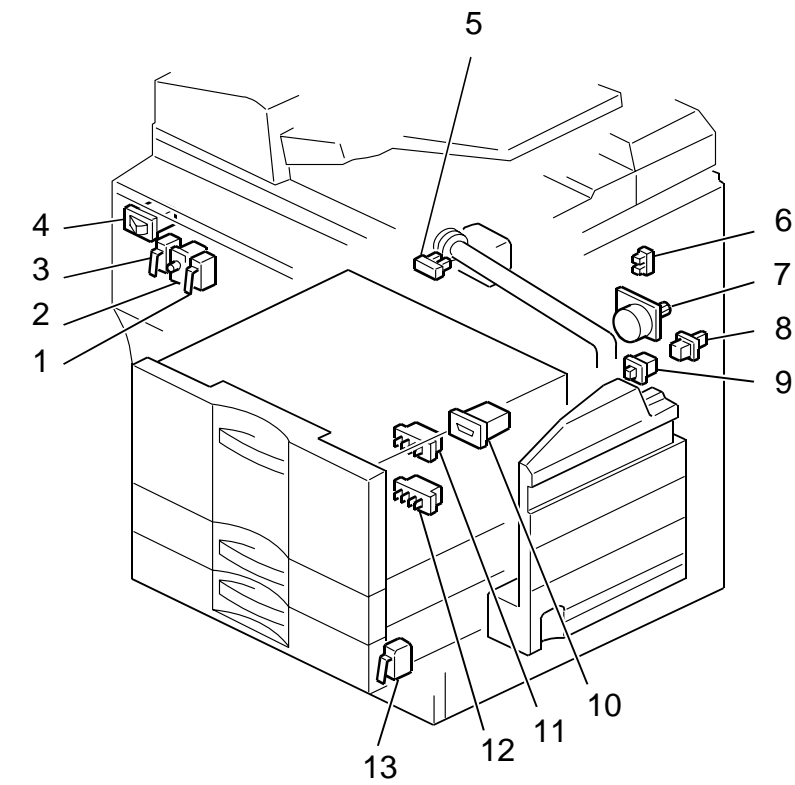


**Fig4** D062V955A.WMF



D062V008.WMF

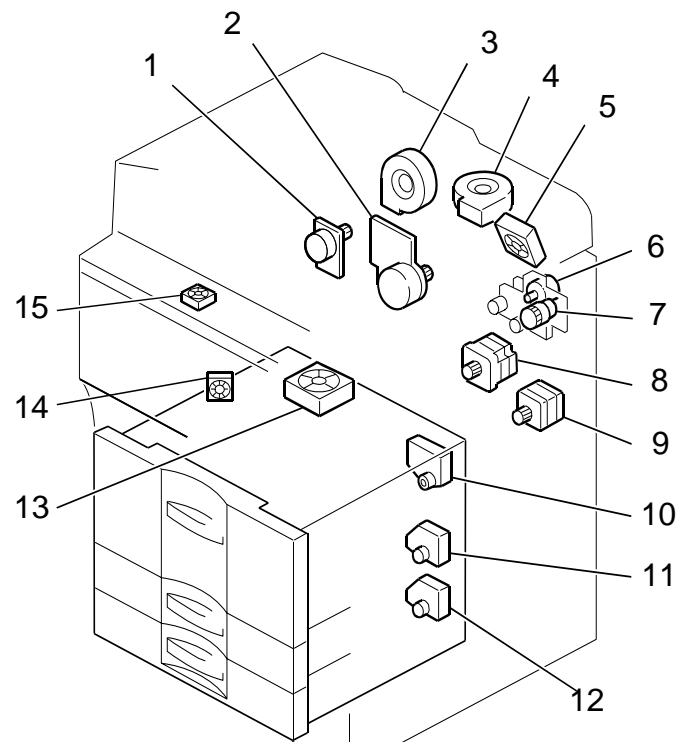
**Fig5**



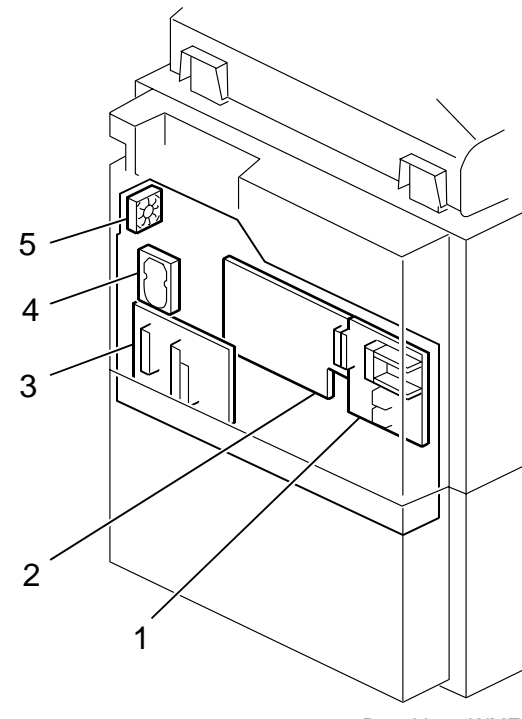
D062V956.WMF

**Fig6**

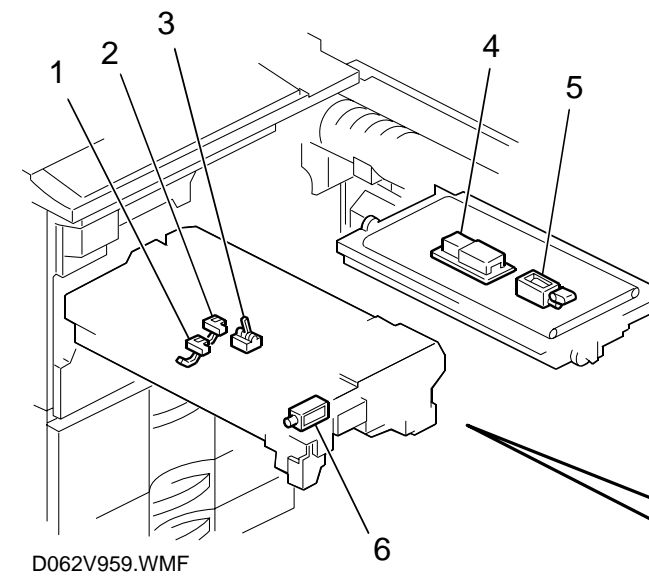
# D062/D063/D065/D066 ELECTRICAL COMPONENT LAYOUT (2/4)



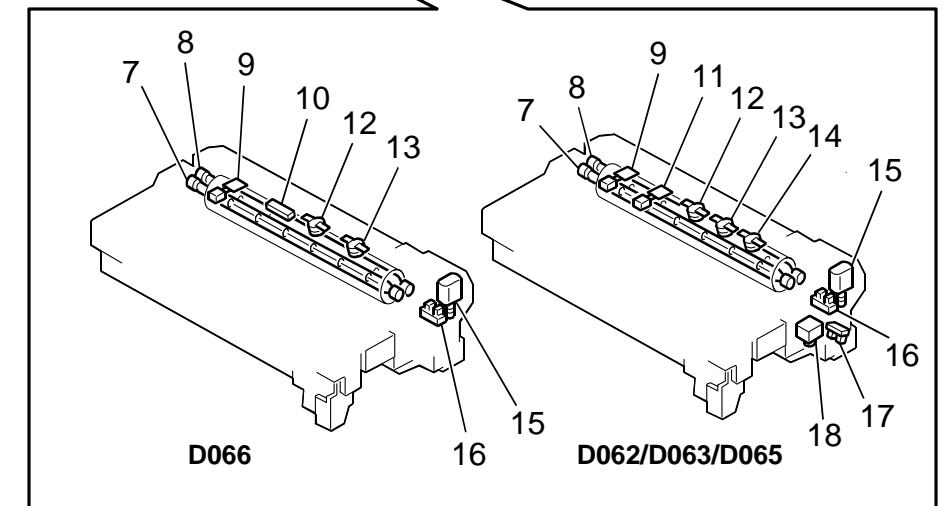
**Fig7** D062V957.WMF



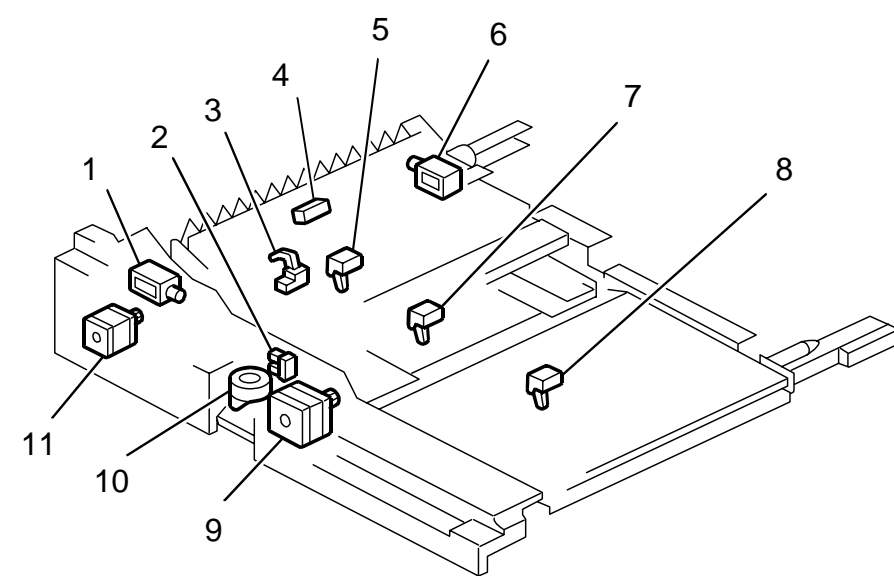
**Fig8** D062V501.WMF



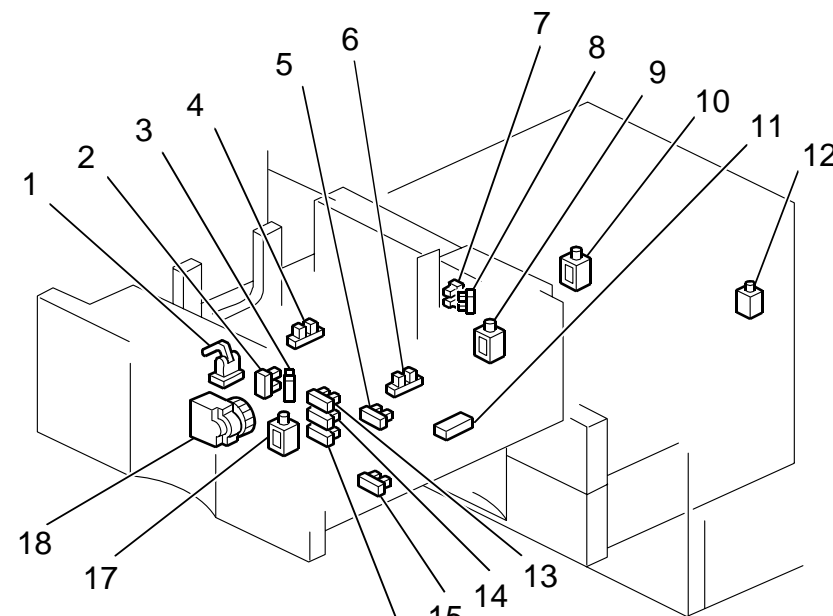
**Fig9** D062V959.WMF



D062V959A.WMF



**Fig10** D062V910.WMF



**Fig11** D062V958.WMF

# D062/D063/D065/D066 ELECTRICAL COMPONENT LAYOUT (3/4)

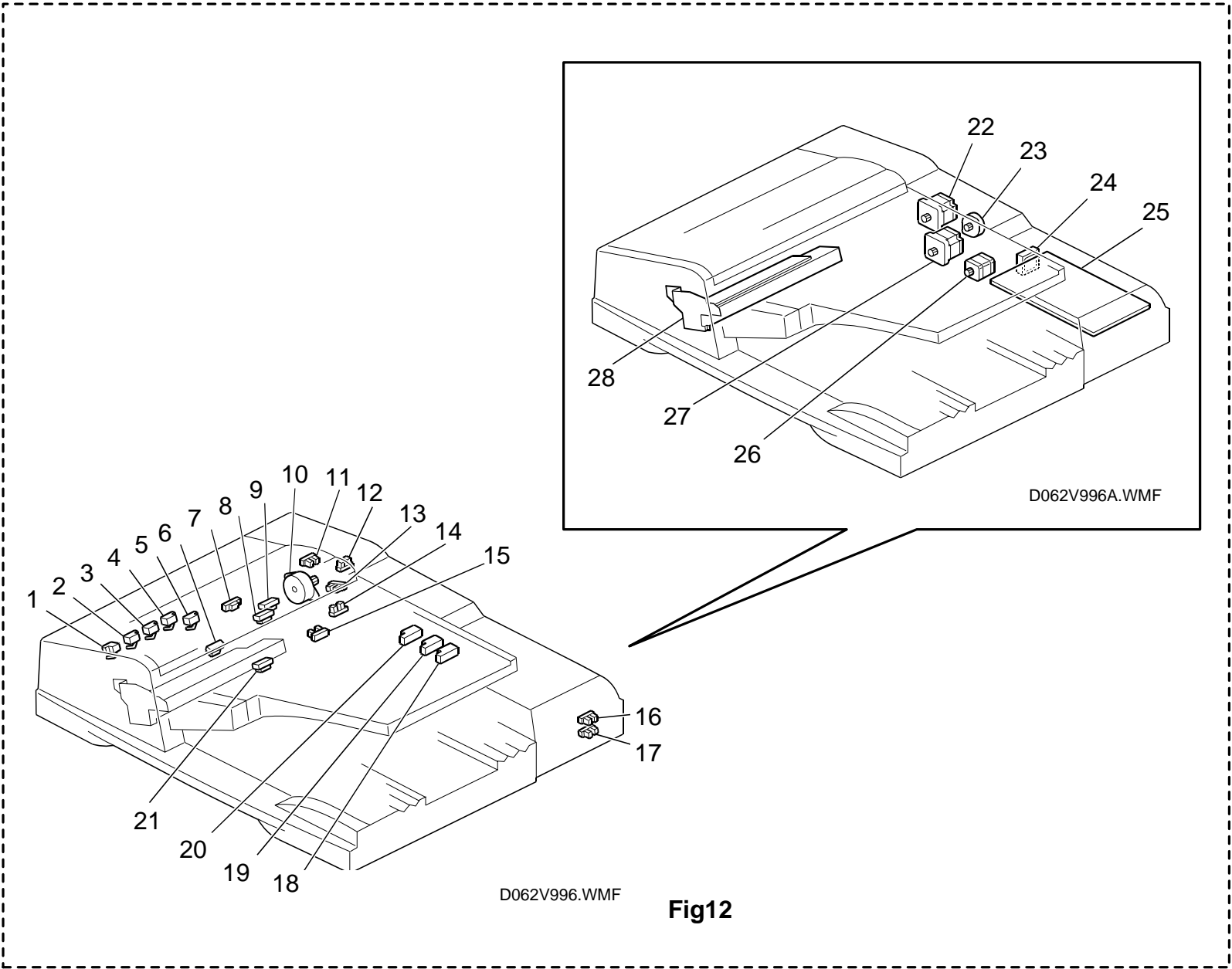
Note; C4a: D062, C4b: D063, C4d: D065, C4e: D066

Symbol	Index No.		Description	P to P	
	a/b/d model	e model		a/b/d model	e model
<b>Motors</b>					
M1	Fig7-2	Fig7-2	Drum	B1 (1/4)	B1 (1/4)
M2	Fig7-1	Fig7-1	Fusing/Exit	B2 (1/4)	B2 (1/4)
M3	Fig7-6	Fig7-6	Development	B2 (1/4)	B2 (1/4)
M4	Fig2-17	Fig2-17	Polygonal Mirror	B5 (1/4)	B5 (1/4)
M5	Fig2-15	Fig2-15	Scanner	B5 (1/4)	B5 (1/4)
M6	Fig2-13	Fig2-13	Scanner Fan Motor - Right	B5 (1/4)	B5 (1/4)
M7	Fig2-6	Fig2-6	Lamp Cooling Fan	C7 (1/4)	C7 (1/4)
M8	Fig5-1	Fig5-1	Toner Supply	F8 (1/4)	F8 (1/4)
M9	Fig7-13	Fig7-13	Duplex Cooling Fan	F8 (1/4)	F8 (1/4)
M10	Fig10-10	Fig10-10	Duplex Jogger	F6 (1/4)	F6 (1/4)
M11	Fig7-14	Fig7-14	PCU Cooling Fan	F5 (1/4)	F5 (1/4)
M12	Fig5-5	Fig5-5	Charge Corona Wire Cleaner	F5 (1/4)	F5 (1/4)
M13	Fig9-15	Fig9-15	Web	F4 (1/4)	F4 (1/4)
M14	Fig9-18	Not used	Fusing Pressure Release Motor	F4 (1/4)	—
M15	Fig7-5	Fig7-5	Drum Cooling Fan	F3 (1/4)	F3 (1/4)
M16	Fig2-14	Fig2-14	Development Unit Cooling Fan 2	F3 (1/4)	F3 (1/4)
M17	Fig2-12	Fig2-12	Development Unit Cooling Fan 1	F3 (1/4)	F3 (1/4)
M18	Fig7-4	Fig7-4	Charge Power Pack Cooling Fan	F3 (1/4)	F3 (1/4)
M19	Fig7-3	Fig7-3	Exhaust Fan	F3 (1/4)	F3 (1/4)
M20	Fig7-15	Fig7-15	Toner Cooling Fan	E2 (1/4)	E2 (1/4)
M21	Fig10-11	Fig10-11	Duplex Inverter	C1 (1/4)	C1 (1/4)
M22	Fig10-9	Fig10-9	Duplex Transport	C1 (1/4)	C1 (1/4)
M23	Fig7-8	Fig7-8	Registration	D1 (1/4)	D1 (1/4)
M24	Fig7-9	Fig7-9	By-pass Feed	D1 (1/4)	D1 (1/4)
M25	Fig4-7	Fig4-7	PSU Cooling Fan 1	B5 (2/4)	B5 (2/4)
M26	Fig4-10	Fig4-10	PSU Cooling Fan 2	B5 (2/4)	B5 (2/4)
M27	Fig11-18	Fig11-18	Rear Fence	F5 (2/4)	F5 (2/4)
M28	Fig6-7	Fig6-7	Toner Collection	F2 (2/4)	F2 (2/4)
M29	Fig7-10	Fig7-10	1st Tray Lift	F2 (2/4)	F2 (2/4)
M30	Fig7-12	Fig7-12	3rd Tray Lift	F2 (2/4)	F2 (2/4)
M31	Fig7-11	Fig7-11	2nd Tray Lift	E2 (2/4)	E2 (2/4)
M32	Fig3-7	Fig3-7	3rd Paper Feed	F2 (2/4)	F2 (2/4)
M33	Fig3-7	Fig3-7	1st Paper Feed	D2 (2/4)	D2 (2/4)
M34	Fig3-7	Fig3-7	2nd Paper Feed	D2 (2/4)	D2 (2/4)
M35	Fig8-5	Fig8-5	Controller Fan	D1 (3/4)	D1 (3/4)
—	Fig3-8	Fig3-8	Lower Relay	—	—
<b>Sensors</b>					
S1	Fig2-2	Fig2-2	Original Width	B6 (1/4)	B6 (1/4)
S2	Fig2-8	Fig2-8	Original Length 2	B6 (1/4)	B6 (1/4)
S3	Fig2-7	Fig2-7	Original Length 1	B6 (1/4)	B6 (1/4)
S4	Fig2-3	Fig2-3	Scanner HP	C6 (1/4)	C6 (1/4)
S5	Fig1-3	Fig1-3	Guide Plate Position	F8 (1/4)	F8 (1/4)
S6	Fig1-6	Fig1-6	By-pass Paper Size	F8 (1/4)	F8 (1/4)
S7	Fig1-8	Fig1-8	Relay	F8 (1/4)	F8 (1/4)
S8	Fig1-2	Fig1-2	By-pass Paper End	F8 (1/4)	F8 (1/4)
S9	Fig1-1	Fig1-1	Registration	F8 (1/4)	F8 (1/4)
S10	Fig5-3	Fig5-3	Toner Density (TD)	F7 (1/4)	F7 (1/4)
S11	Fig5-4	Fig5-4	Toner End	F7 (1/4)	F7 (1/4)
S12	Fig6-5	Fig6-5	Toner Collection Coil	F6 (1/4)	F6 (1/4)
S13	Fig10-8	Fig10-8	Duplex Transport 3	F6 (1/4)	F6 (1/4)
S14	Fig10-7	Fig10-7	Duplex Transport 2	F6 (1/4)	F6 (1/4)
S15	Fig10-2	Fig10-2	Duplex Jogger HP	F6 (1/4)	F6 (1/4)
S16	Fig10-5	Fig10-5	Duplex Transport 1	F6 (1/4)	F6 (1/4)
S17	Fig10-4	Fig10-4	Duplex Inverter	F6 (1/4)	F6 (1/4)
S18	Fig10-5	Fig10-5	Duplex Entrance	F5 (1/4)	F5 (1/4)
S19	Fig5-2	Fig5-2	Drum Potential	F5 (1/4)	F5 (1/4)
S20	Fig5-7	Fig5-7	Image Density (ID)	F5 (1/4)	F5 (1/4)
S21	Fig9-1	Fig9-1	Paper Exit	F5 (1/4)	F5 (1/4)

Symbol	Index No.		Description	P to P	
	a/b/d model	e model		a/b/d model	e model
S22	Fig9-2	Fig9-2	Exit Unit Entrance	F4 (1/4)	F4 (1/4)
S23	Fig9-16	Fig9-16	Web End	F4 (1/4)	F4 (1/4)
S24	Fig19-17	Not used	Fusing Pressure Release Sensor	F4 (1/4)	—
S25	Fig6-6	Fig6-6	Toner Collection Motor	E4 (1/4)	E4 (1/4)
S26	Fig9-3	Fig9-3	Fusing Exit	B8 (2/4)	D8 (2/4)
S27	Fig11-3	Fig11-3	Front Side Fence Open	D5 (2/4)	D5 (2/4)
S28	Fig11-2	Fig11-2	Front Side Fence Close	D5 (2/4)	D5 (2/4)
S29	Fig11-8	Fig11-8	Rear Side Fence Open	D5 (2/4)	D5 (2/4)
S30	Fig11-7	Fig11-7	Rear Side Fence Close	D5 (2/4)	D5 (2/4)
S31	Fig11-15	Fig11-15	Right Tray Down	E5 (2/4)	E5 (2/4)
S32	Fig11-5	Fig11-5	Near End	E5 (2/4)	E5 (2/4)
S33	Fig11-13	Fig11-13	Paper Height 1	E5 (2/4)	E5 (2/4)
S34	Fig11-14	Fig11-14	Paper Height 2	E5 (2/4)	E5 (2/4)
S35	Fig11-16	Fig11-16	Paper Height 3	E5 (2/4)	E5 (2/4)
S36	Fig11-11	Fig11-11	Right Tray Paper	E5 (2/4)	E5 (2/4)
S37	Fig11-4	Fig11-4	Rear Fence HP	F5 (2/4)	F5 (2/4)
S38	Fig11-6	Fig11-6	Rear Fence Return	F5 (2/4)	F5 (2/4)
S39	Fig11-1	Fig11-1	Left Tray Paper End	F5 (2/4)	F5 (2/4)
S40	Fig3-4	Fig3-4	3rd Tray Lift	B2 (2/4)	B2 (2/4)
S41	Fig3-3	Fig3-3	3rd Paper End	B2 (2/4)	B2 (2/4)
S42	Fig3-2	Fig3-2	3rd Vertical Transport	B2 (2/4)	B2 (2/4)
S43	Fig3-1	Fig3-1	3rd Paper Feed	B2 (2/4)	B2 (2/4)
S44	Fig3-4	Fig3-4	2nd Tray Lift	B2 (2/4)	B2 (2/4)
S45	Fig3-3	Fig3-3	2nd Paper End	B2 (2/4)	B2 (2/4)
S46	Fig3-2	Fig3-2	2nd Vertical Transport	B2 (2/4)	B2 (2/4)
S47	Fig3-1	Fig3-1	2nd Paper Feed	B2 (2/4)	B2 (2/4)
S48	Fig4-8	Fig4-8	Temperature Sensor	A2 (2/4)	A2 (2/4)
S49	Fig3-4	Fig3-4	1st Tray Lift	A2 (2/4)	A2 (2/4)
S50	Fig3-3	Fig3-3	1st Paper End	A2 (2/4)	A2 (2/4)
S51	Fig3-2	Fig3-2	1st Vertical Transport	A2 (2/4)	A2 (2/4)
S52	Fig3-1	Fig3-1	1st Paper Feed	A2 (2/4)	A2 (2/4)
<b>Switches</b>					
SW1	Fig6-2	Fig6-2	Front Door Safety	C1 (1/4)	C1 (1/4)
SW2	Fig6-1	Fig6-1	Front Cover Safety 1	C9 (1/4)	C9 (1/4)
SW3	Fig6-3	Fig6-3	Front Cover Safety 2	C9 (1/4)	C9 (1/4)
SW4	Fig6-4	Fig6-4	Main	A5 (2/4)	A5 (2/4)
SW5	Fig6-13	Fig6-13	Lower Front Door Safety	D4 (2/4)	D4 (2/4)
SW6	Fig6-8	Fig6-8	Toner Overflow	F2 (2/4)	F2 (2/4)
SW7	Fig6-9	Fig6-9	Toner Collection Bottle Set	F2 (2/4)	F2 (2/4)
SW8	Fig6-11	Fig6-11	2nd Paper Size	E2 (2/4)	E2 (2/4)
SW9	Fig6-12	Fig6-12	3rd Paper Size	E2 (2/4)	E2 (2/4)
<b>Solenoids</b>					
SOL1	Fig1-7	Fig1-7	Guide Plate	F8 (1/4)	F8 (1/4)
SOL2	Fig1-4	Fig1-4	By-pass Pick-up	F8 (1/4)	F8 (1/4)
SOL3	Fig9-5	Fig9-5	Transfer Belt Lift	F7 (1/4)	F7 (1/4)
SOL4	Fig10-6	Fig10-6	Duplex Junction Gate	F5 (1/4)	F5 (1/4)
SOL5	Fig10-1	Fig10-1	Reverse Trigger Roller	F5 (1/4)	F5 (1/4)
SOL6	Fig9-6	Fig9-6	Exit Junction Gate	F4 (1/4)	F4 (1/4)
SOL7	Fig11-17	Fig11-17	Front Side Fence	E5 (2/4)	E5 (2/4)
SOL8	Fig11-9	Fig11-9	Rear Side Fence	E5 (2/4)	E5 (2/4)
SOL9	Fig11-12	Fig11-12	Right Tray Lock	F4 (2/4)	F4 (2/4)
SOL10	Fig11-10	Fig11-10	Left Tray Lock	F4 (2/4)	F4 (2/4)
SOL11	Fig3-5	Fig3-5	3rd Separation Roller	B2 (2/4)	B2 (2/4)
SOL12	Fig3-6	Fig3-6	3rd Pick-up	B2 (2/4)	B2 (2/4)
SOL13	Fig3-5	Fig3-5	2nd Separation Roller	B2 (2/4)	B2 (2/4)
SOL14	Fig3-6	Fig3-6	2nd Pick-up	B2 (2/4)	B2 (2/4)
SOL15	Fig3-5	Fig3-5	1st Separation Roller	A2 (2/4)	A2 (2/4)
SOL16	Fig3-6	Fig3-6	1st Pick-up	A2 (2/4)	A2 (2/4)

Symbol	Index No.		Description	P to P	
	a/b/d model	e model		a/b/d model	e model
<b>Magnetic Clutches</b>					
MC1	Fig1-5	Fig1-5	By-pass Feed	F8 (1/4)	F8 (1/4)
—	Fig7-7	Fig7-7	Toner Supply	B2 (1/4)	B2 (1/4)
<b>PCBs</b>					
PCB1	Fig2-1	Fig2-1	OPU	A4 (1/4)	A4 (1/4)
PCB2	Fig8-2	Fig8-2	IPU (Image Processing Control Unit)	B7 (1/4)	B7 (1/4)
PCB3	Fig2-18	Fig2-18	Polygon Motor Drive Board	B4 (1/4)	B4 (1/4)
PCB4	Fig4-13	Fig4-13	CNB (Connection Board)	C3 (1/4)	C3 (1/4)
PCB5	Fig2-5	Fig2-5	Lamp Stabilizer	B5 (1/4)	B5 (1/4)
PCB6	Fig2-11	Fig2-11	SIOB	C5 (1/4)	C5 (1/4)
PCB7	Fig2-9	Fig2-9	SBU (Sensor Board Unit)	C7 (1/4)	C7 (1/4)
PCB8	Fig2-10	Fig2-10	SCNB	C7 (1/4)	C7 (1/4)
PCB9	Fig2-19	Fig2-19	LDB (Laser Drive Board)	C8 (1/4)	C8 (1/4)
PCB10	Fig4-1	Fig4-1	BCU (Base Engine Control Unit)	D5 (1/4)	D5 (1/4)
PCB11	Fig4-3	Fig4-3	DRB (Drive Board)	D1 (1/4)	D1 (1/4)
PCB12	Fig4-5	Fig4-5	PSU	B6 (2/4)	B6 (2/4)
PCB13	Fig8-3	Fig8-3	Controller PSU	E6 (2/4)	E6 (2/4)
PCB14	Fig4-6	Fig4-6	PFB (Paper Feed Board)	A2 (2/4)	A2 (2/4)
PCB15	Fig8-1	Fig8-1	Controller	D2 (3/4)	D2 (3/4)
<b>Lamps</b>					
L1	Fig2-4	Fig2-4	Exposure Lamp	B5 (1/4)	B5 (1/4)
L2	Fig5-6	Fig5-6	PTL	F5 (1/4)	F5 (1/4)
L3	Fig5-8	Fig5-8	Quenching Lamp	F5 (1/4)	F5 (1/4)
L4	Fig9-7	Fig9-7	Fusing Lamp 1	B8 (2/4)	D8 (2/4)
L5	Fig9-8	Fig9-8	Fusing Lamp 2	B8 (2/4)	D8 (2/4)
<b>Power Packs</b>					
PP1	Fig9-4	Fig9-4	Transfer	F7 (1/4)	F7 (1/4)
PP2	Fig4-4	Fig4-4	Development	E7 (1/4)	E7 (1/4)
PP3	Fig4-2	Fig4-2	Charge	E4 (1/4)	E4 (1/4)
<b>Others</b>					
CO1	—	—	Key Counter (Option)	E2 (1/4)	E2 (1/4)
TC	Fig6-10	Fig6-10	Mechanical Counter	F7 (1/4)	F7 (1/4)
H1	Fig4-12	Fig4-12	Drum	A6 (2/4)	A6 (2/4)
H2	—	—	Scanner (Option)	A6 (2/4)	A6 (2/4)
H3	Fig4-9	Fig4-9	Lower Tray	A6 (2/4)	A6 (2/4)
H4	Fig4-11	Fig4-11	Upper Tray	A6 (2/4)	A6 (2/4)
LSD1	Fig2-16	Fig2-16	Laser Synchronization Detector	B4 (1/4)	B4 (1/4)
TH1	Not used	Fig9-10	Thermistor 1 (not contact the hot roller)	—	D8 (2/4)
TH1	Fig9-11	Not used	Thermistor 1 (contact the hot roller)	B8 (2/4)	—
TH2	Fig9-9	Fig9-9	Thermistor 2	B8 (2/4)	D8 (2/4)
TS1	Fig9-12	Fig9-12	Thermostat 1	B8 (2/4)	D8 (2/4)
TS2	Fig9-13	Fig9-13	Thermostat 2	B8 (2/4)	D8 (2/4)
TS3	Fig9-14	Not used	Thermostat 3	B8 (2/4)	—
HDD1	Fig8-4	Fig8-4	HDD	B2 (3/4)	B2 (3/4)

# ADF (FOR D062/D063/D065/D066) ELECTRICAL COMPONENT LAYOUT (4/4)



Symbol	Index No.	Description	P to P
<b>Motors</b>			
M1	Fig12-22	Feed	A10 (4/4)
M2	Fig12-27	Transport	B10 (4/4)
M3	Fig12-26	Exit	B10 (4/4)
M4	Fig12-23	Bottom Plate	C10 (4/4)
M5	Fig12-10	Pick-up	D10 (4/4)
—	Fig12-24	ADF Fan	—
<b>Sensors</b>			
S1	Fig12-1	Original Width 5	A1 (4/4)
S2	Fig12-2	Original Width 4	B1 (4/4)
S3	Fig12-3	Original Width 3	B1 (4/4)
S4	Fig12-4	Original Width 2	B1 (4/4)
S5	Fig12-5	Original Width 1	B1 (4/4)
S6	Fig12-11	Pick-up Roller HP	B3 (4/4)
S7	Fig12-7	Interval	C1 (4/4)
S8	Fig12-9	Skew Correction	C1 (4/4)
S9	Fig12-8	Seperation Sensor	C1 (4/4)
S10	Fig12-21	Exit	D1 (4/4)
S11	Fig12-6	Registration	D1 (4/4)
S12	Fig12-14	Original Set	D1 (4/4)
S13	Fig12-15	Bottom Plate HP	E1 (4/4)
S14	Fig12-12	Feed Cover	E1 (4/4)
S15	Fig12-13	Bottom Plate Position	E1 (4/4)
S16	Fig12-18	Original Length 1	F1 (4/4)
S17	Fig12-19	Original Length 2	F1 (4/4)
S18	Fig12-20	Original Length 3	F1 (4/4)
S19	Fig12-16	DF Position	G3 (4/4)
S20	Fig12-17	APS Start	G3 (4/4)
<b>Solenoids</b>			
SOL1	—	Stamp Solenoid	E10 (4/4)
<b>PCBs</b>			
PCB1	Fig12-25	DF Main	D6 (4/4)
<b>Lamps</b>			
L1	Fig12-28	CIS	G9 (4/4)